

# What can AI learn from the brain?

## Past, Present and Future

Simon Thorpe

Emeritus CNRS Research Director

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Toulouse, France

NICE 2025

26 March 2025, Heidelberg, Germany

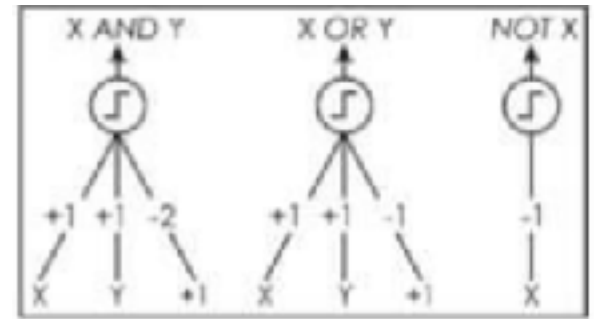


# Plan

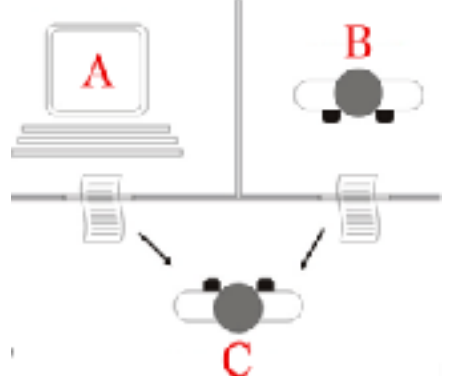
- A brief history of the links between AI and Neuroscience
- Brain inspiration for AI
  - Artificial Neurones
  - Feedforward architectures for recognition and categorisation
  - Spike based coding
- “Terabrain” Systems
  - Simulating 68 billion neurons on a Mac!

# AI and the Brain

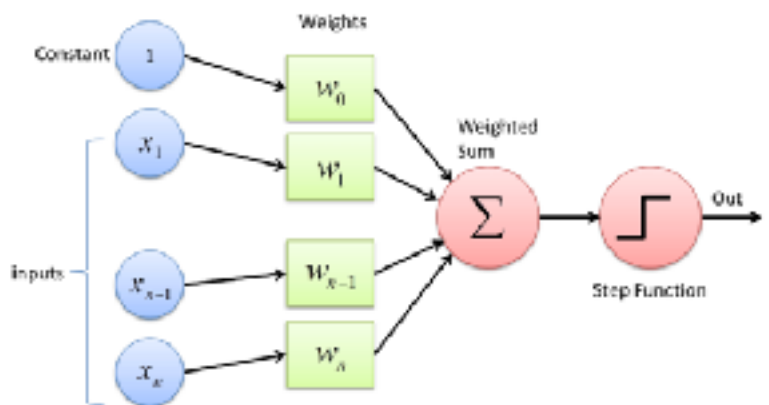
Warren McCulloch & Walter Pitts  
Threshold Logic Units



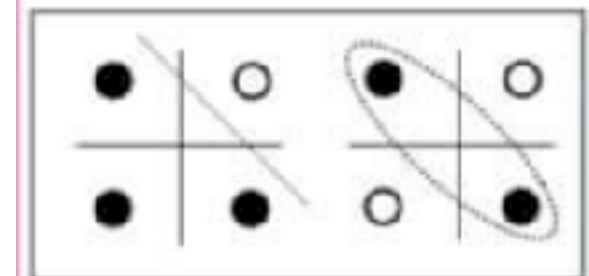
Alan Turing  
Turing Test



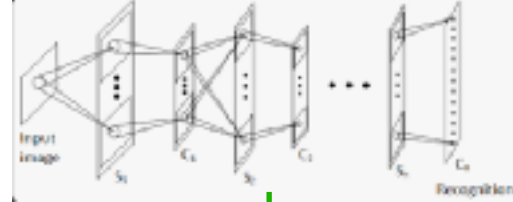
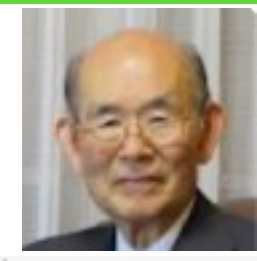
Frank Rosenblatt  
Perceptron



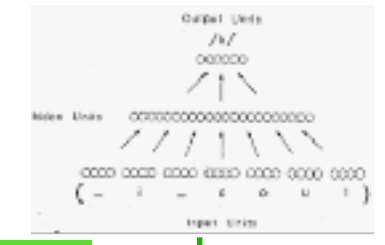
Marvin Minsky & Seymour Papert  
XOR problem



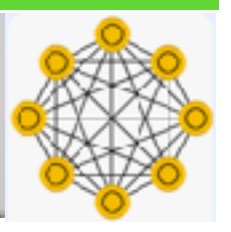
Kunihiko Fukushima  
Neocognitron



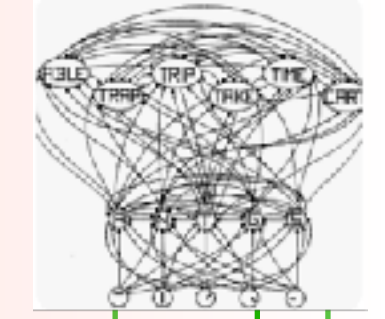
Terry Sejnowski  
NetTalk



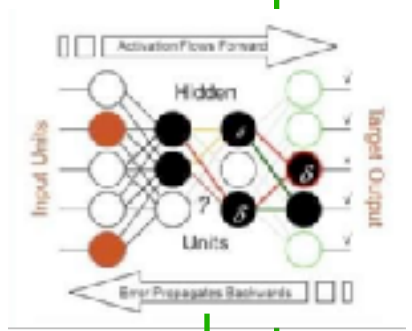
John Hopfield  
Hopfield Net



Dave Rumelhart & Jay McClelland  
Interactive Activation



Geoff Hinton, Yann Lecun  
Back Propagation



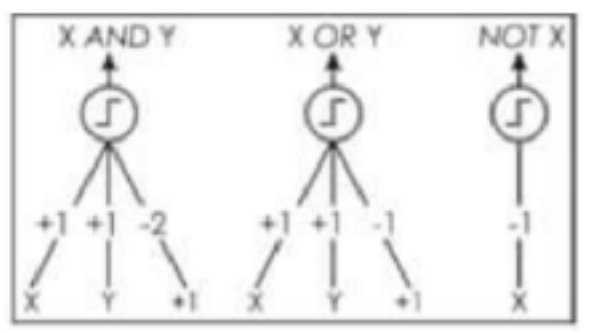
First NeuroAI Winter  
"Good Old Fashioned AI"  
Symbolic Logic  
Expert Systems

1940                      1950                      1960                      1970                      1980                      1990

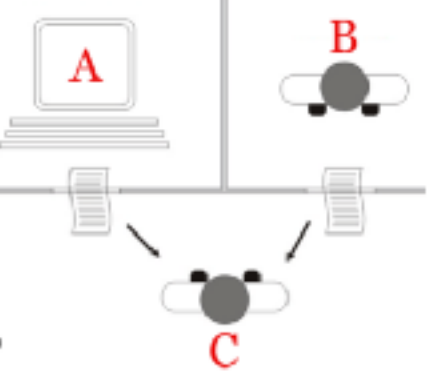


# AI and the Brain

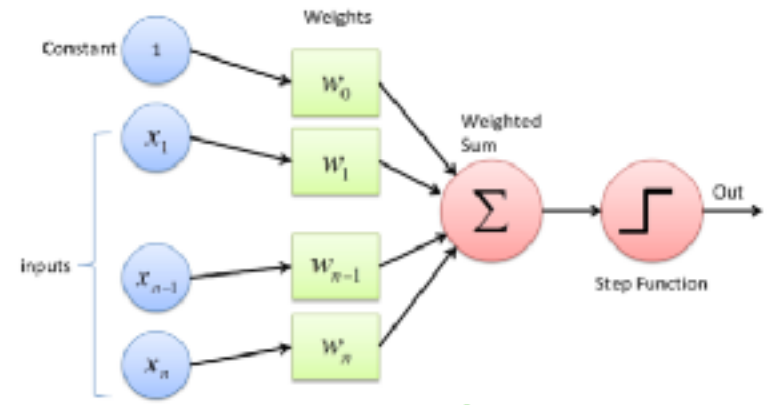
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Threshold Logic Units



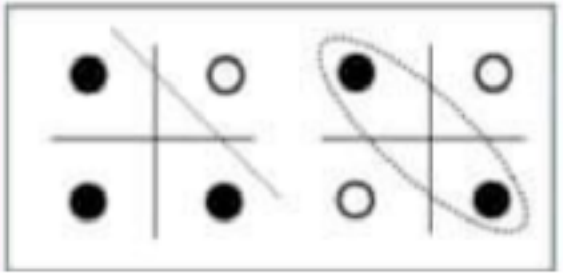
Alan Turing  
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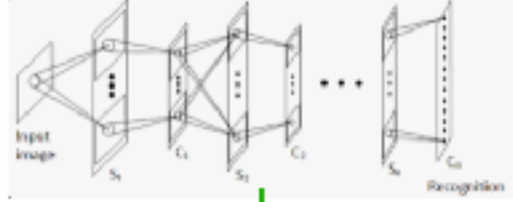
Frank Rosenblatt  
Perceptron



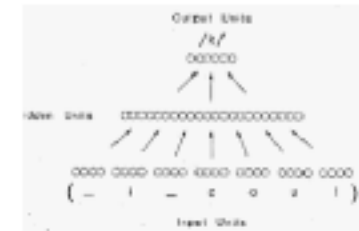
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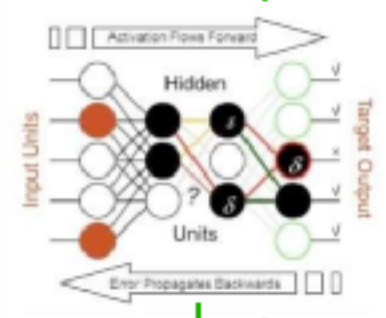
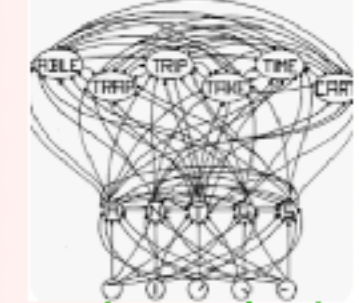
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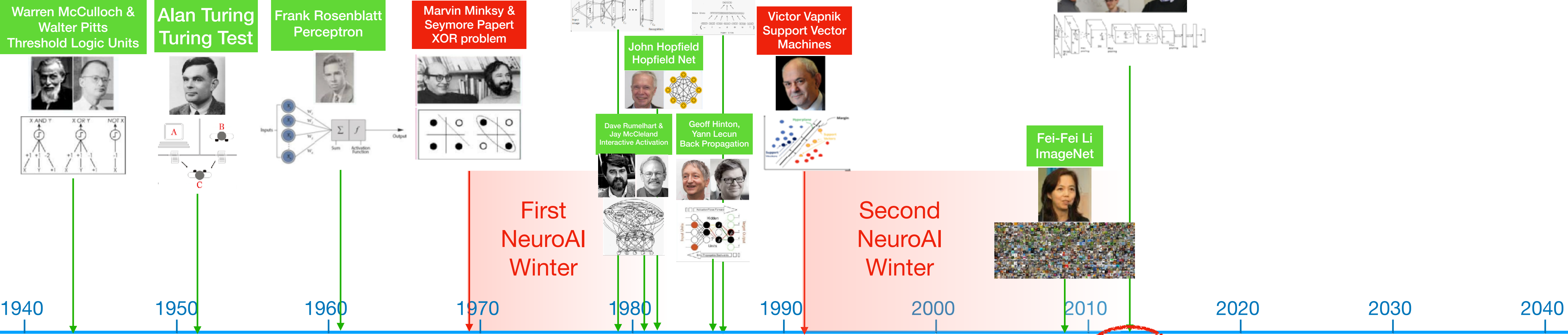


First NeuroAI Winter  
"Good Old Fashioned AI"  
Symbolic Logic  
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1940 1950 1960 1970 1980 1990



# AI and the Brain

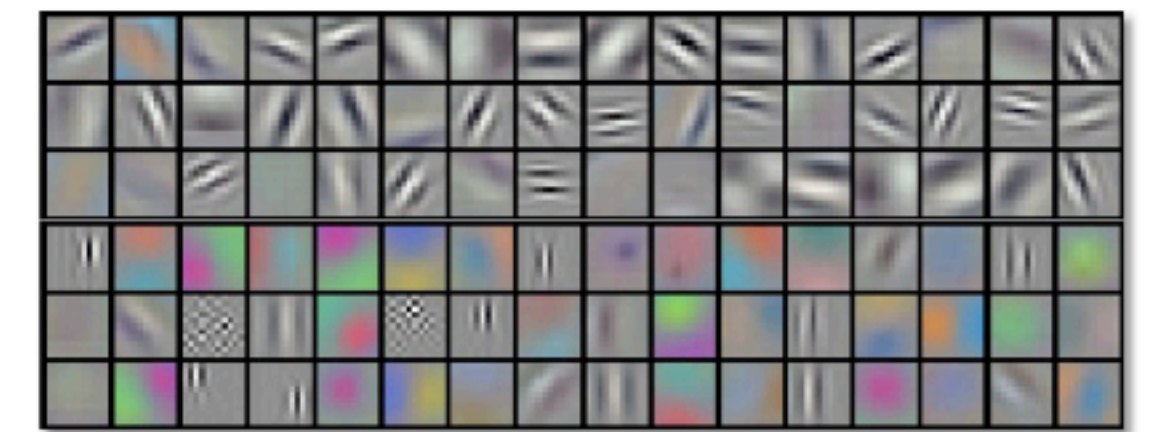
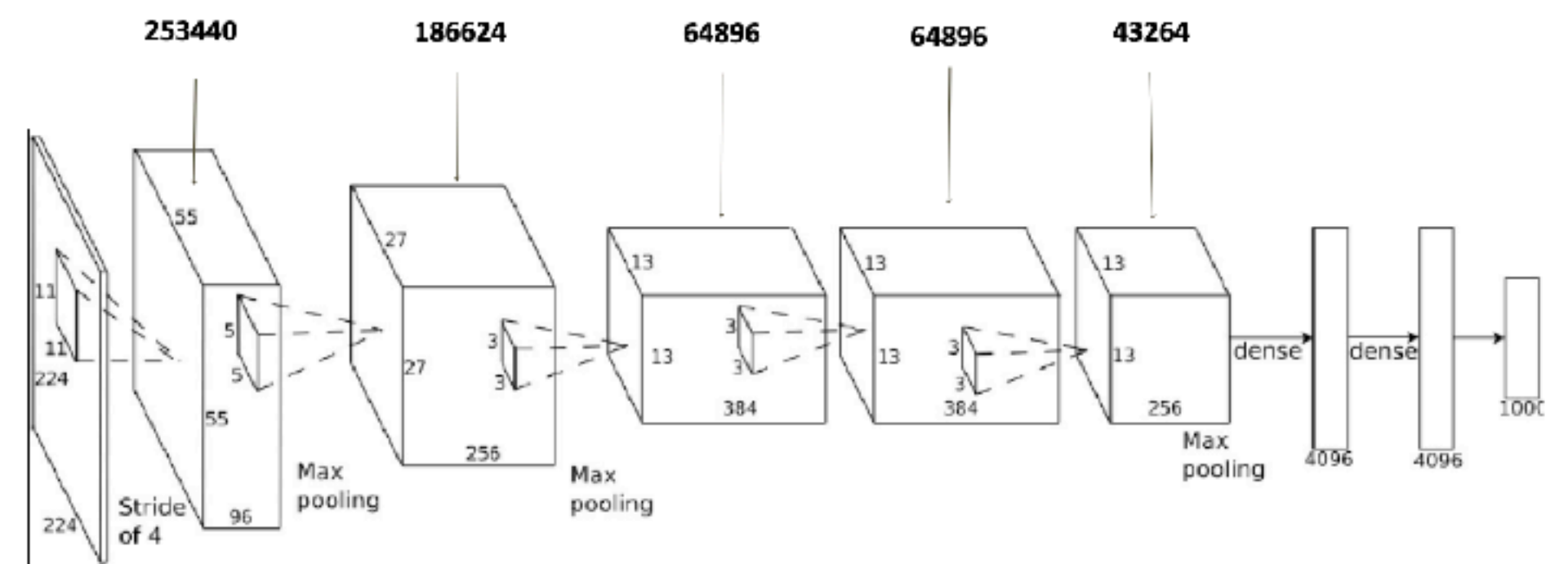


## ImageNet Classification with Deep Convolutional Neural Networks

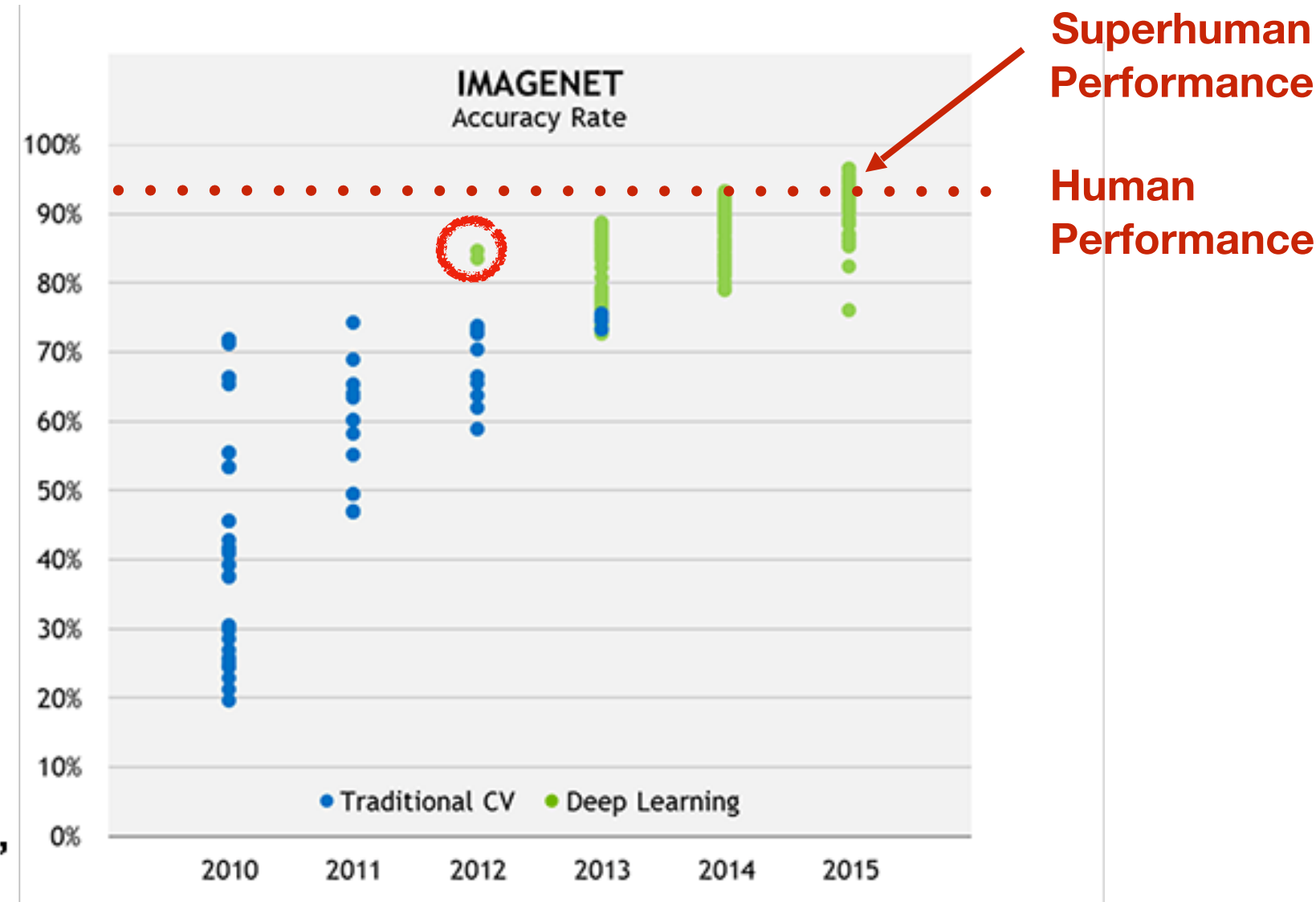
Alex Krizhevsky  
University of Toronto  
kriz@cs.utoronto.ca

Ilya Sutskever  
University of Toronto  
ilya@cs.utoronto.ca

Geoffrey E. Hinton  
University of Toronto  
hinton@cs.utoronto.ca

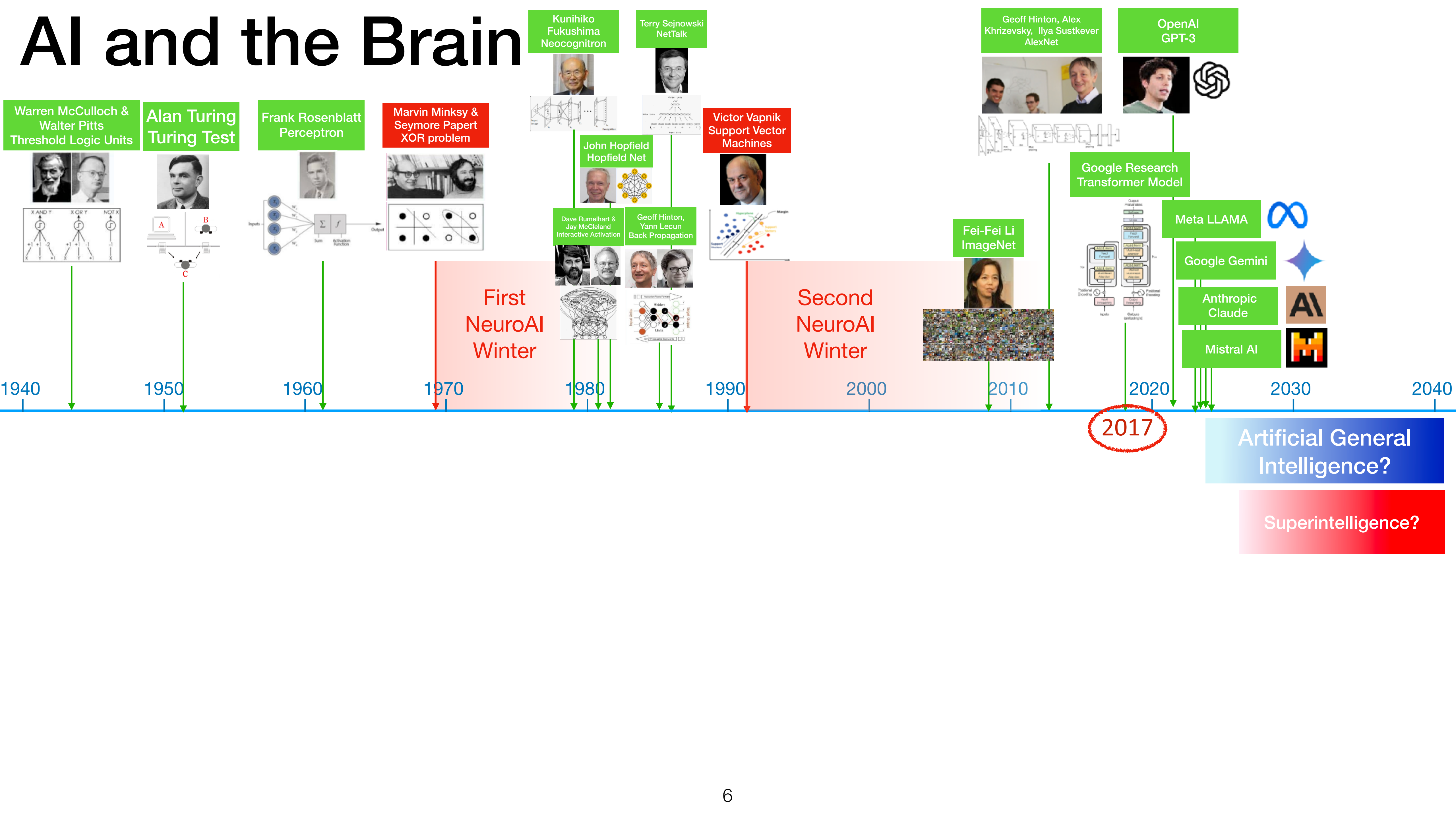


- 650,000 “neurons”
- 60,000,000 parameters
- 630,000,000 “synapses”



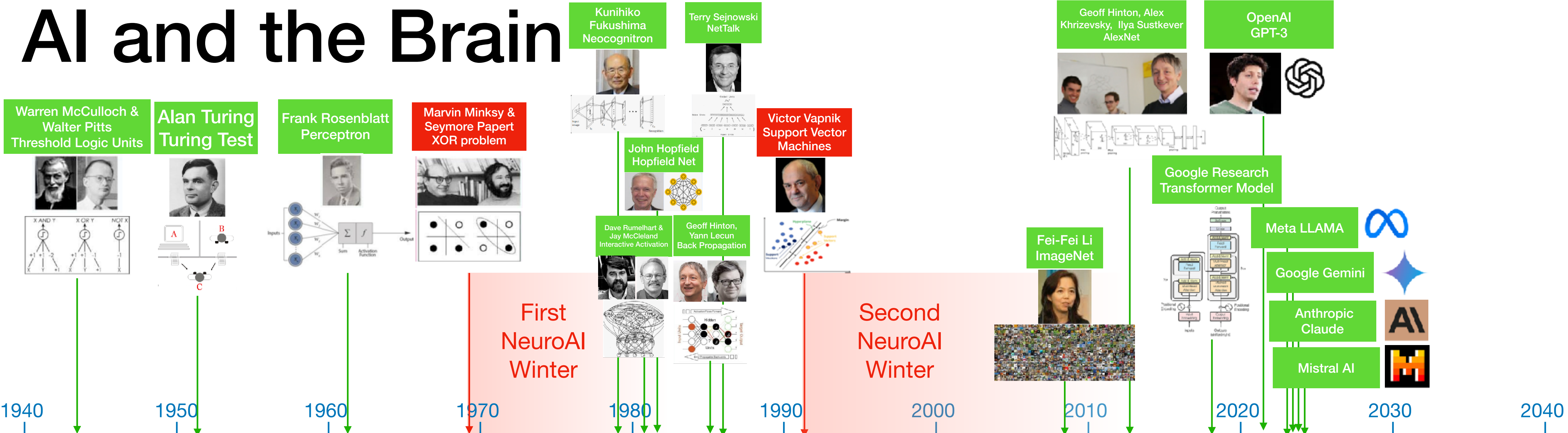
• More than 173 000 citations!

# AI and the Brain



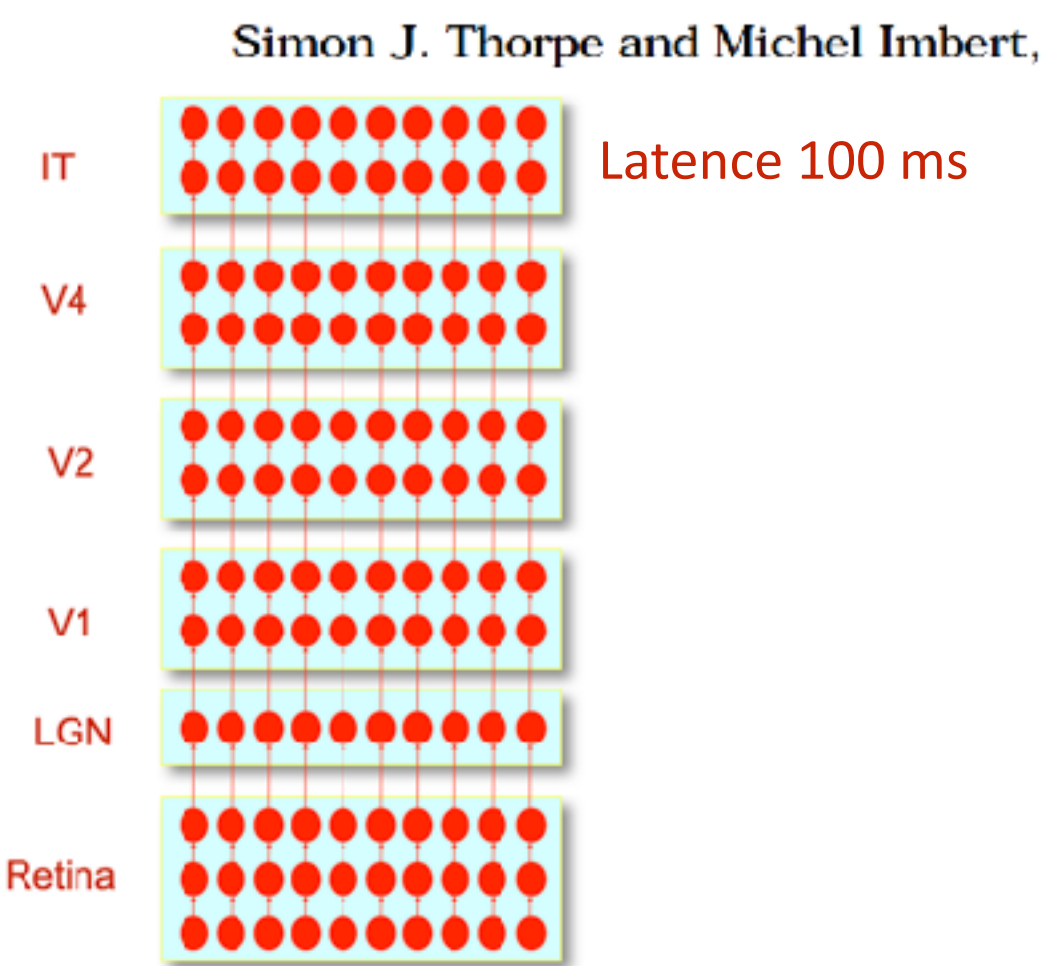


# AI and the Brain

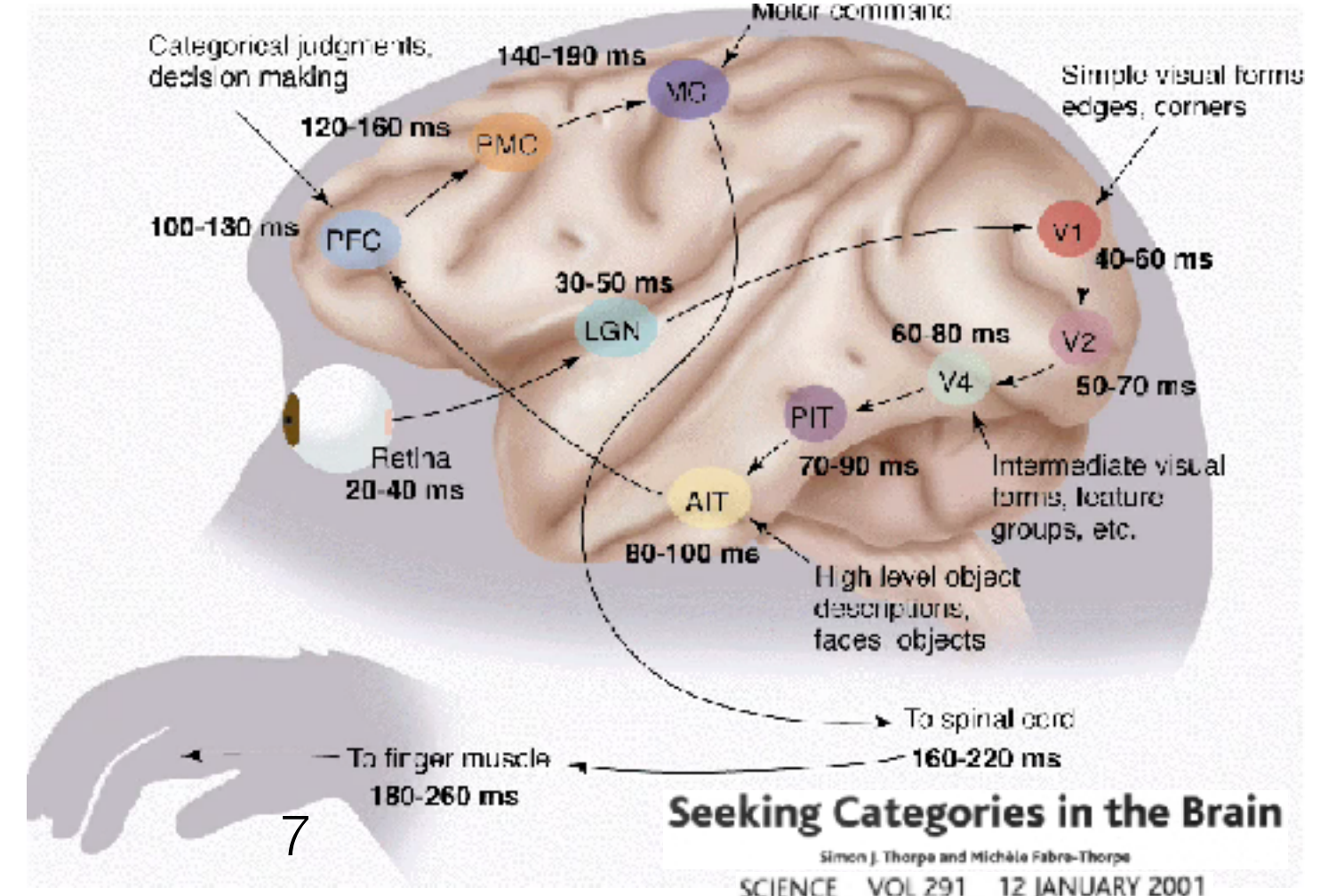


## My contributions?

BIOLOGICAL CONSTRAINTS ON CONNECTIONIST MODELLING



**Speed of processing in the human visual system**  
 Simon Thorpe, Denis Fize & Catherine Marlot  
 Centre de Recherche Cerveau & Cognition, UMR 5549, 31062 Toulouse, France  
 NATURE · VOL 381 · 6 JUNE 1996

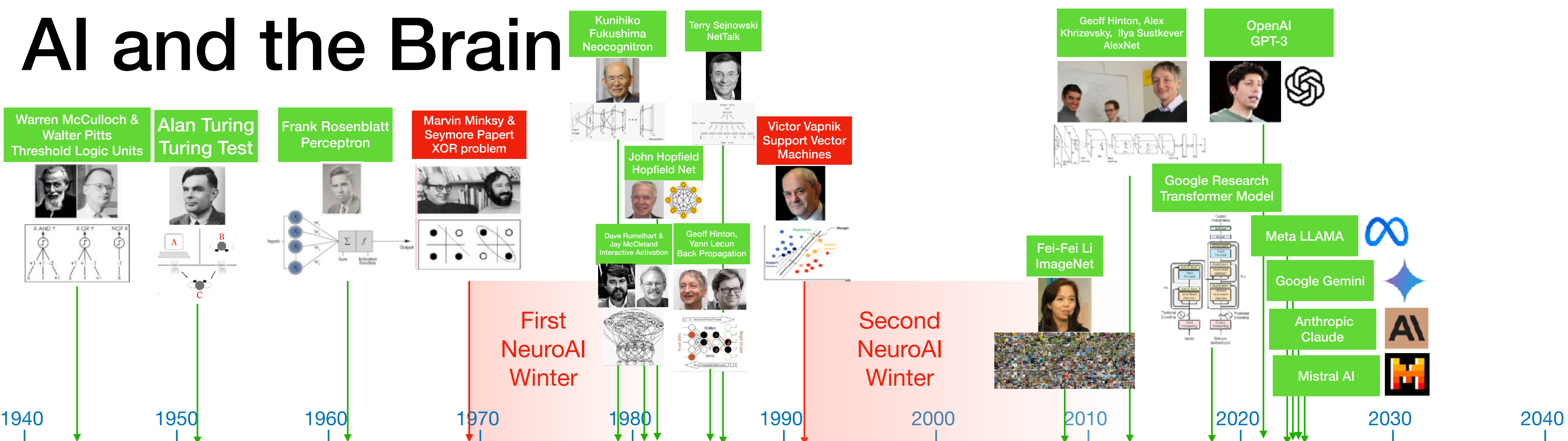


RESEARCH ARTICLE | BIOLOGICAL SCIENCES |  
**Rapid natural scene categorization in the near absence of attention**  
 Fei Fei Li, Rodolphe VanRullen, Christof Koch, and Pietro Perona  
 June 20, 2002 · 99 (14): 9596-9601 | <https://doi.org/10.1073/pnas.092277599>

**Ultra-Rapid Visual Categorisation and Feedforward processing**



# AI and the Brain

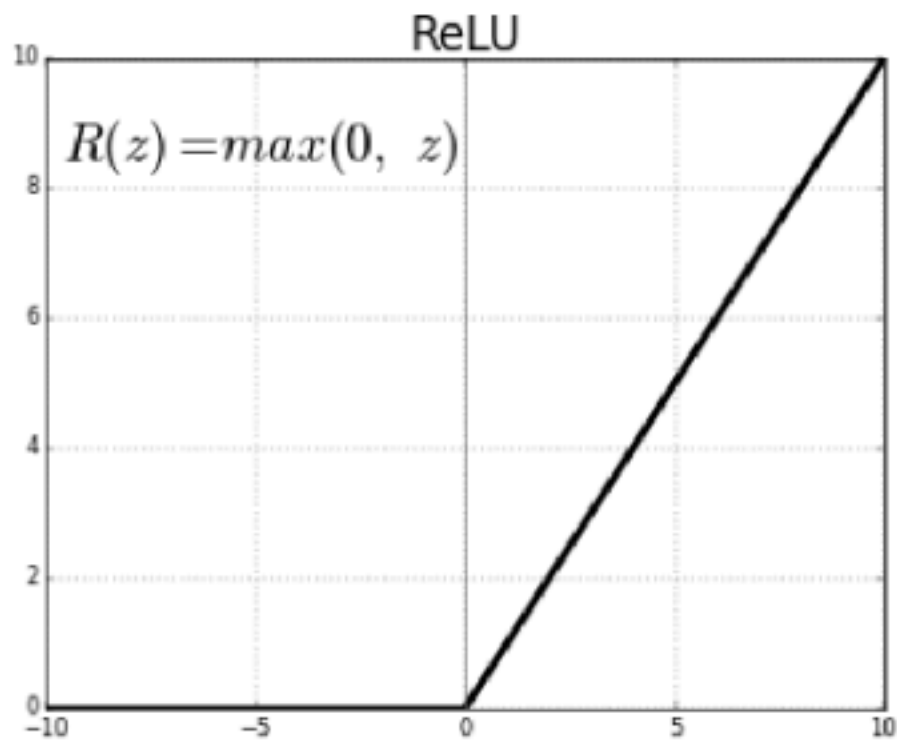
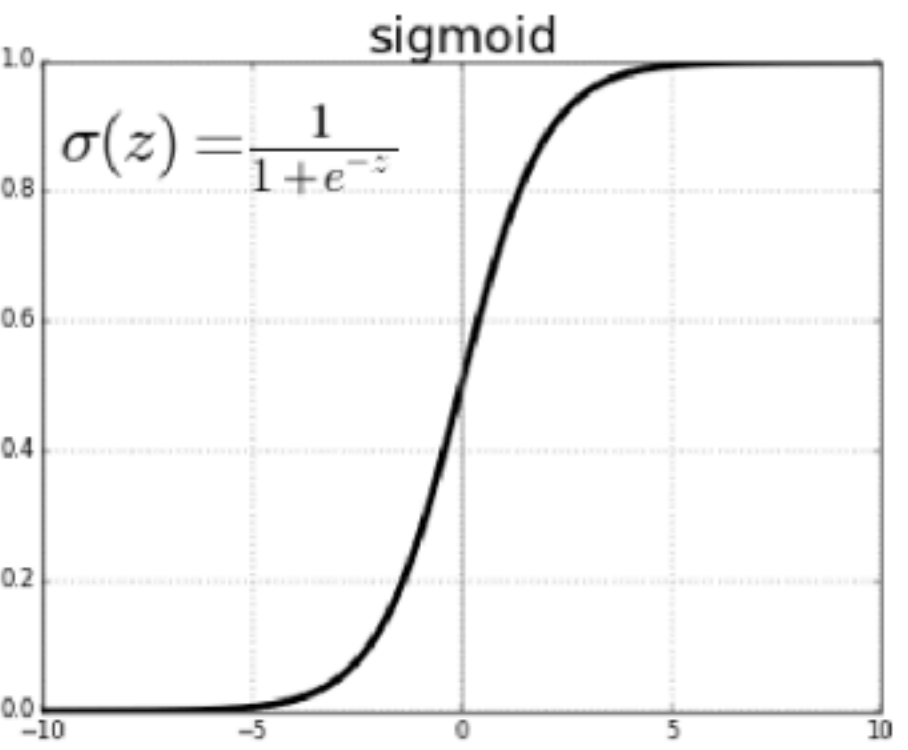
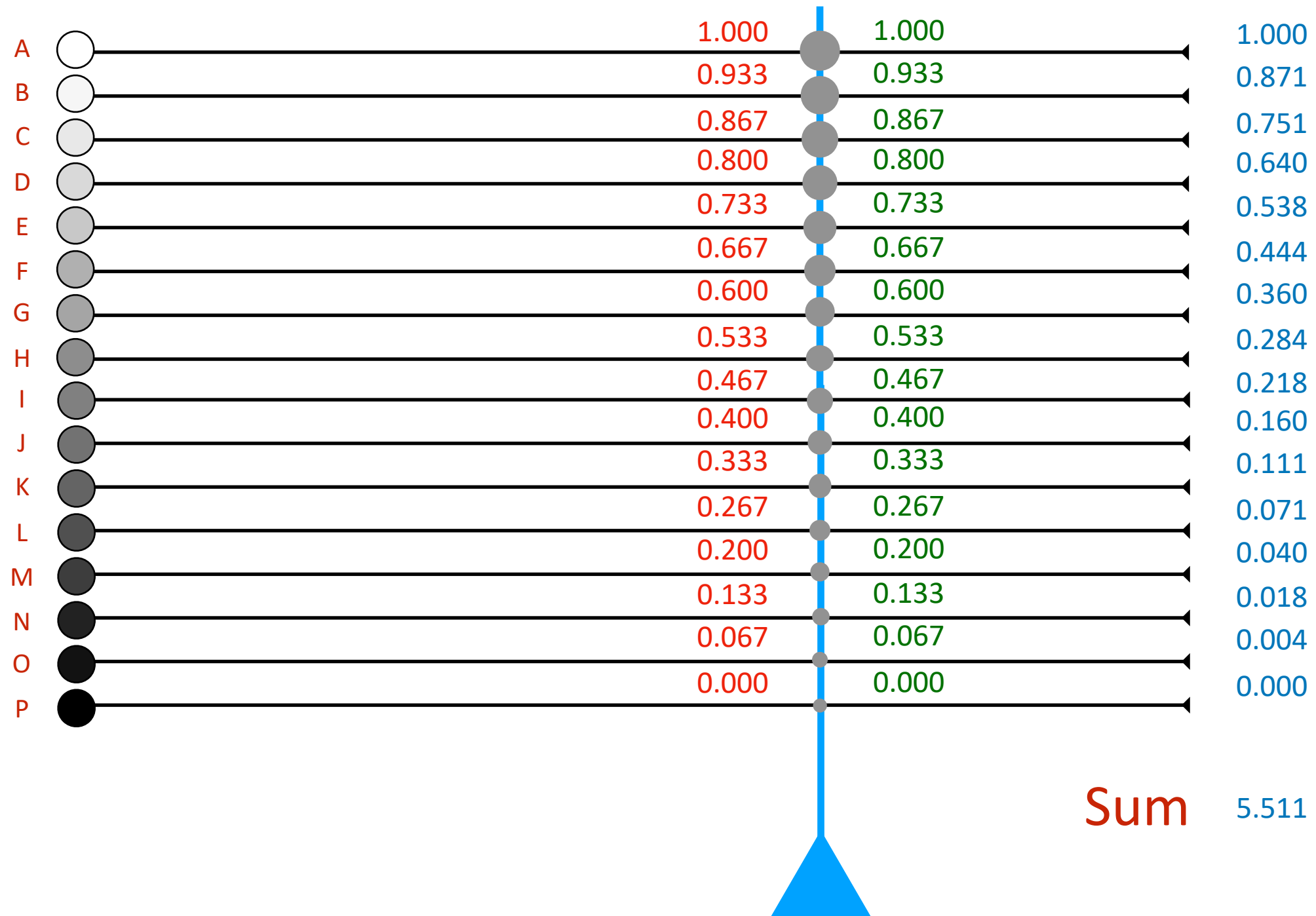


Have we learned all there is to learn from the brain ?

Should AI continue to develop without knowing more about brain mechanisms?

**NO!!!**

# Classic Neural Networks



- Used in AlexNet, Transformers etc.
- Activations coded with floating point numbers
- Weights coded with floating point numbers
- Final Activation :
  - $(1.000 \times 1.000) + (0.933 \times 0.933) + \dots + (0.000 \times 0.000) = 5.511$
- Output function (e.g. sigmoid or ReLU)
- Very expensive computations
  - 16 multiplications
  - 16 additions
  - Output function



# The energy problem

## To simulate the human brain

- 86 billion neurones
- 7000 synapses per neurone
- To recalculate every millisecond
  - $8.6E+10 * 7.0E+3 * 1.0E+3 = 6.02E+17$  FLOPS
  - 600 PetaFLOPS
- Only 3 supercomputers can do it (all in the USA)
- Over 20 MW!
- Jupiter Exascale Computer in Jülich, Germany



• **The Brain is 1 million times more efficient (20W)**



Rank	System	Cores	Rmax (PFlop/s)	Rpeak (PFlop/s)	Power (kW)
1	Frontier - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE DOE/SC/Oak Ridge National Laboratory United States	8,699,904	1,194.00	1,679.82	22,703
2	Aurora - HPE Cray EX - Intel Exascale Compute Blade, Xeon CPU Max 9470 52C 2.4GHz, Intel Data Center GPU Max, Slingshot-11, Intel DOE/SC/Argonne National Laboratory United States	4,742,808	585.34	1,059.33	24,687
3	Eagle - Microsoft NDv5, Xeon Platinum 8480C 48C 2GHz, NVIDIA H100, NVIDIA Infiniband NDR, Microsoft Microsoft Azure United States	1,123,200	561.20	846.84	
4	Supercomputer Fugaku - Supercomputer Fugaku, A64FX 48C 2.2GHz, Tofu interconnect D, Fujitsu RIKEN Center for Computational Science Japan	7,630,848	442.01	537.21	29,899
5	LUMI - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE EuroHPC/CSC Finland	2,752,704	379.70	531.51	7,107



## • What's the secret?

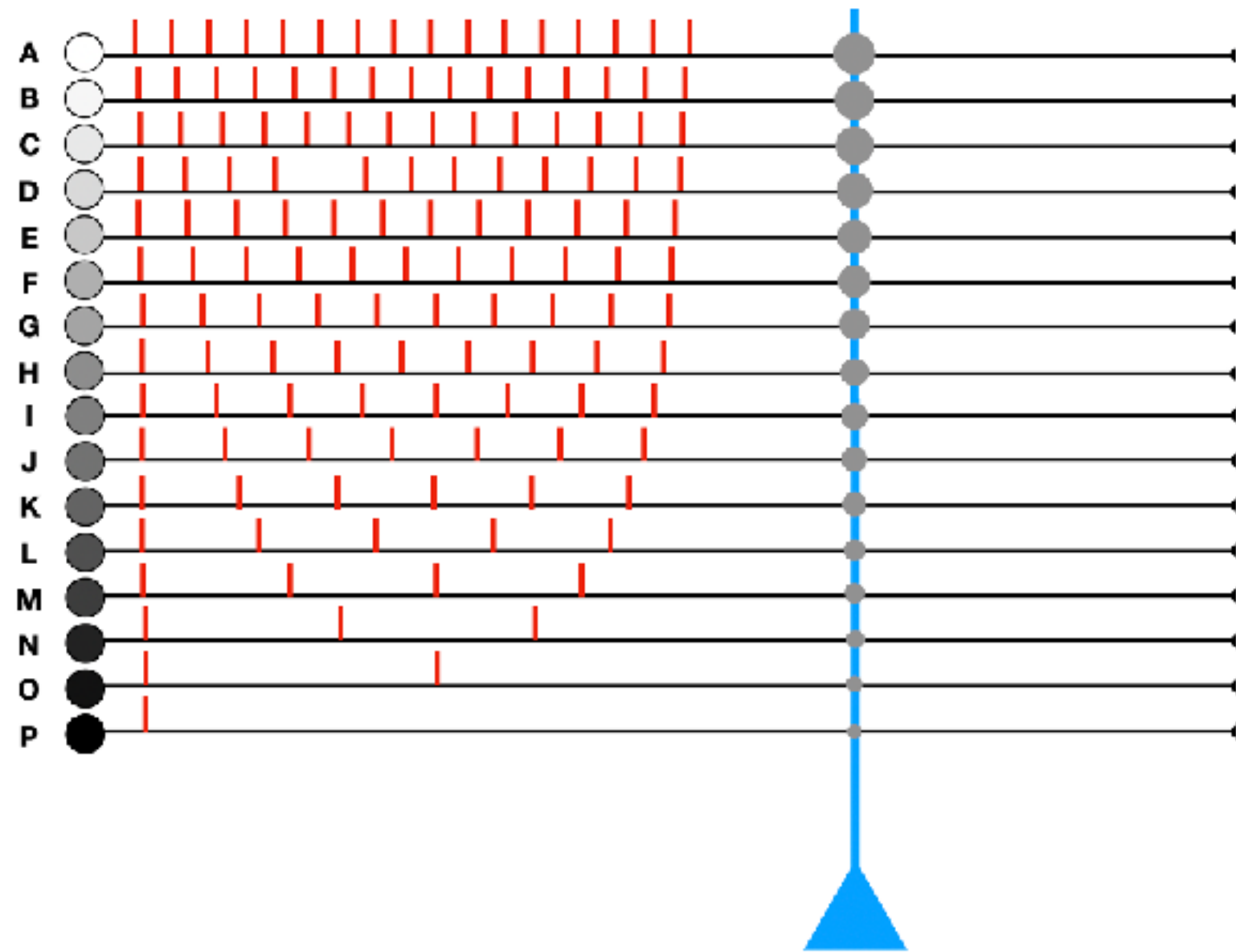
- Neuromorphic Computing
- Analog computation?
- Memristors?
- Spikes!



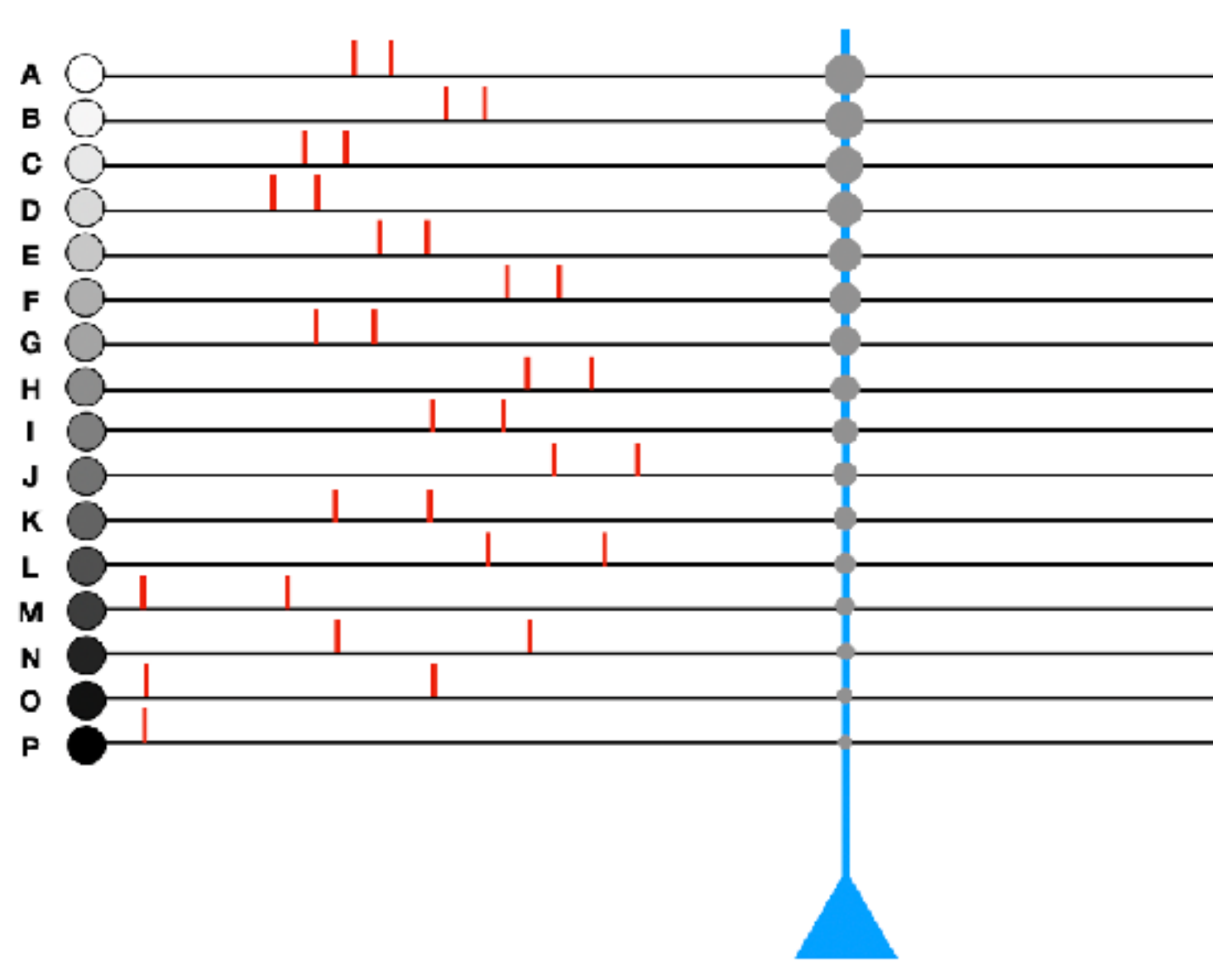
# Spikes - The standard view

- 99% of research?
- It's the rate of firing that counts
- Several methods

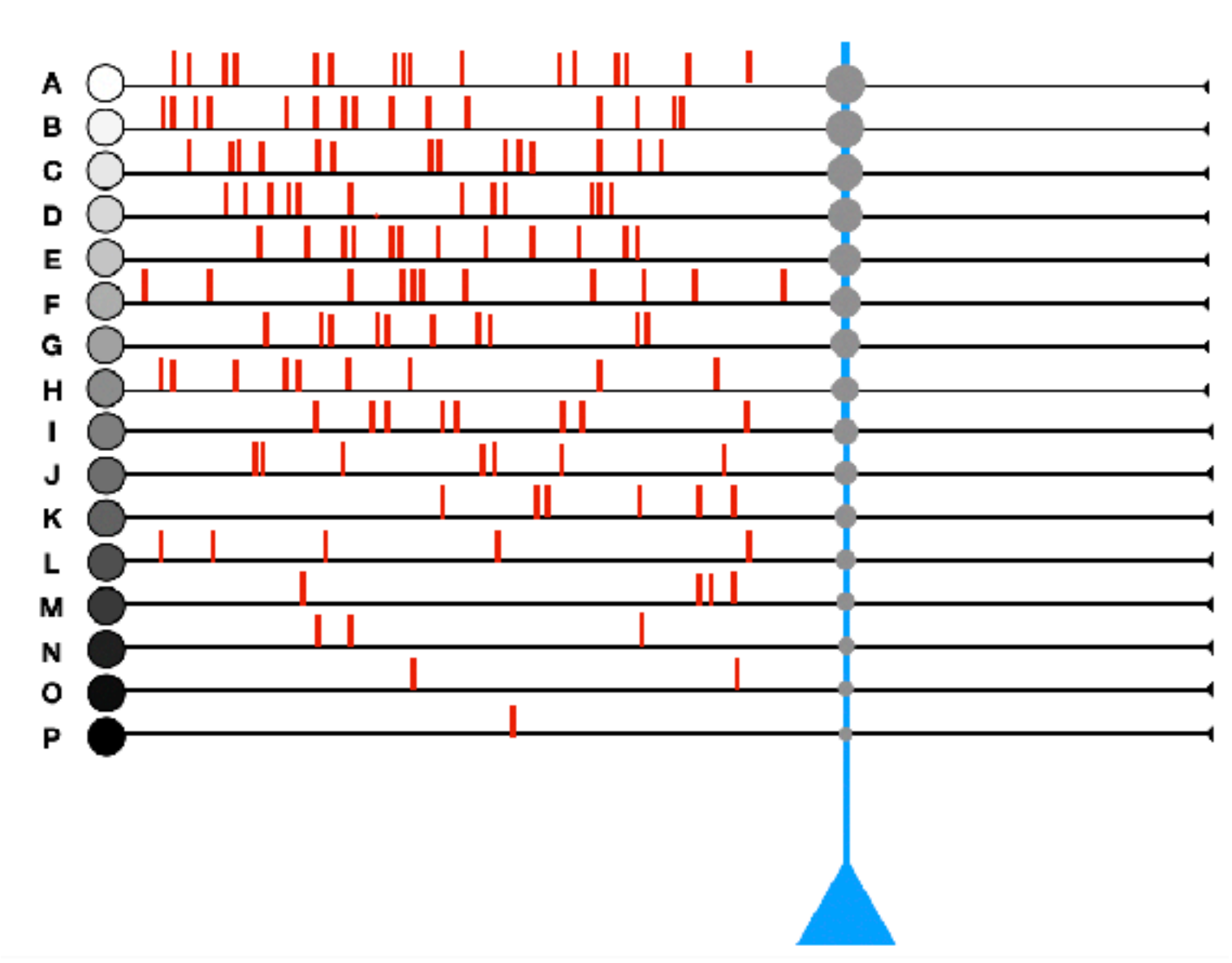
**A. Count spikes in a fixed times**



**B. Interspike Intervals**



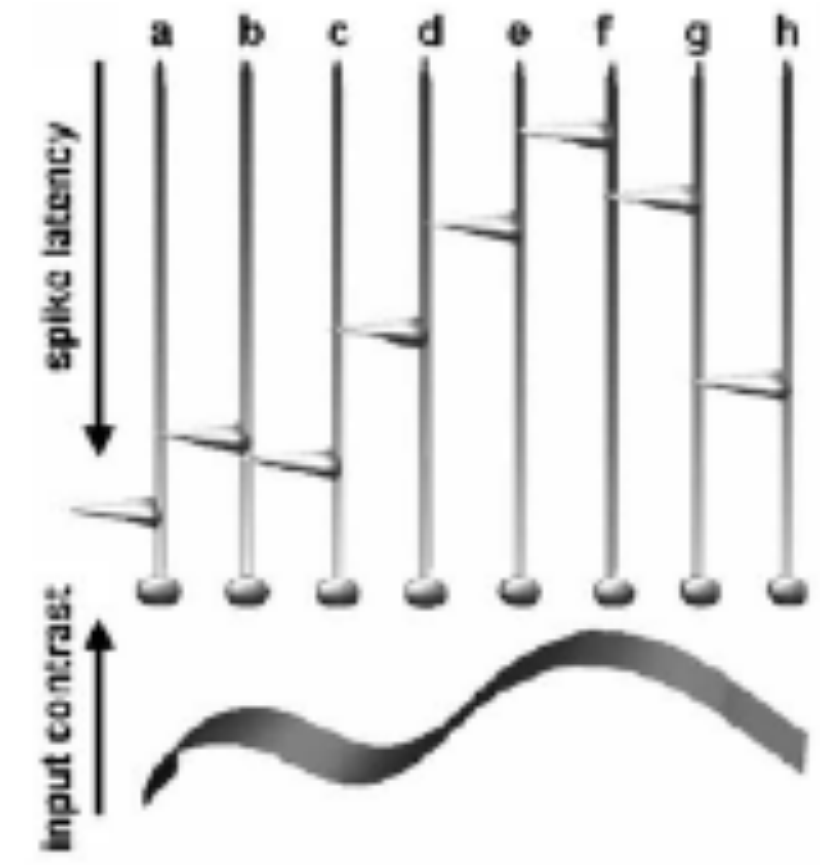
**C. Poisson Coding**



• All really bad!

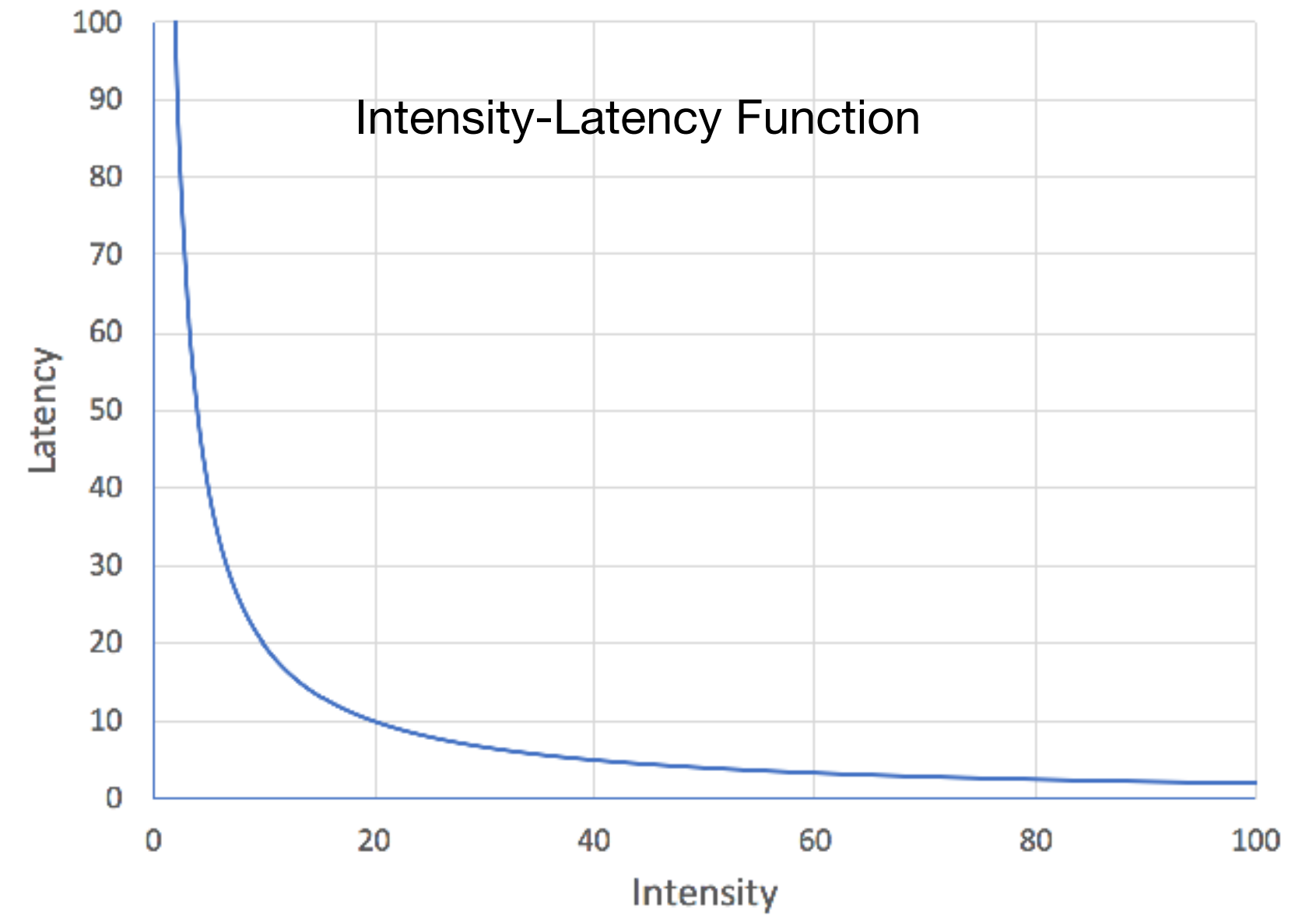
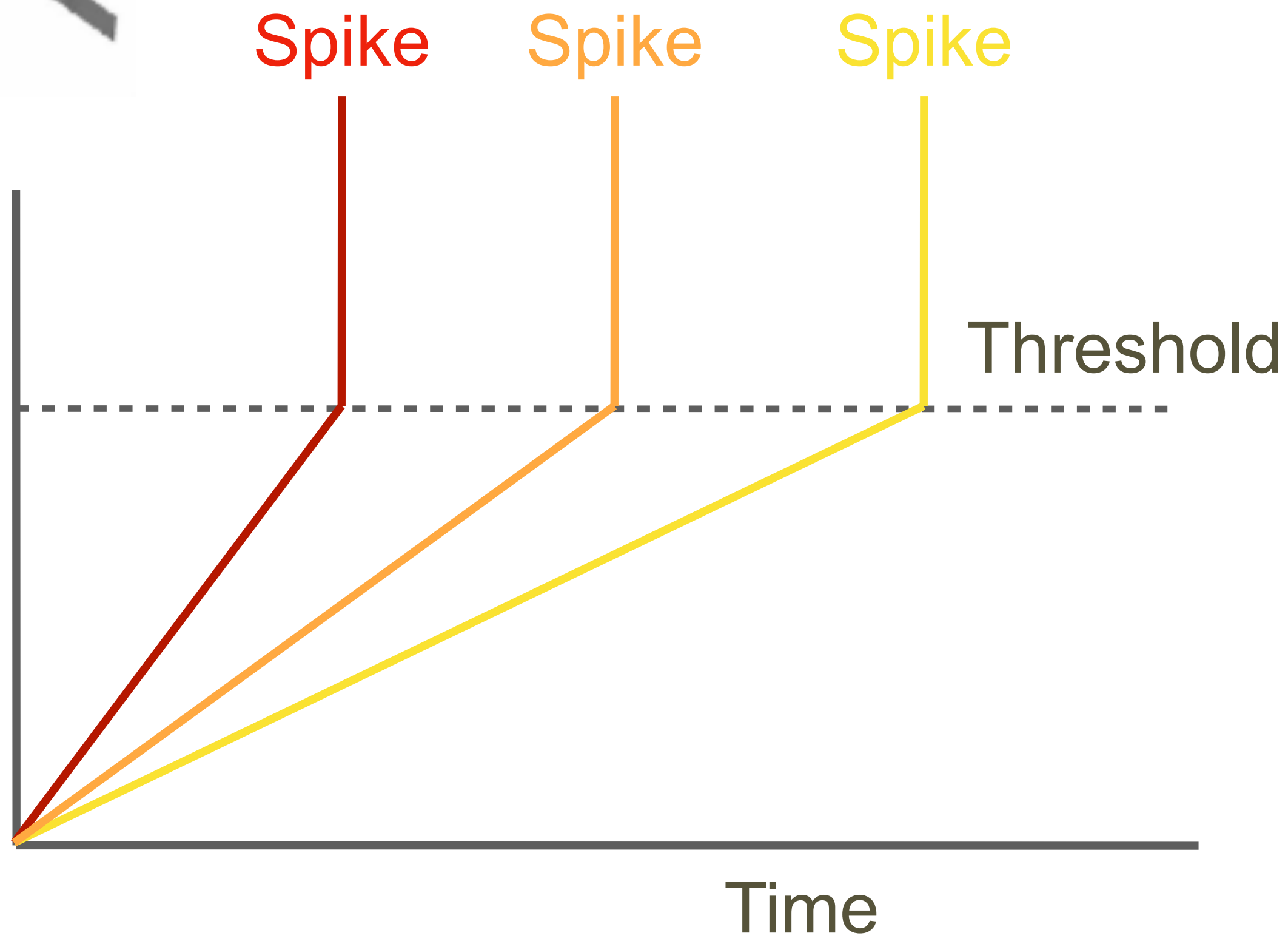
# The Alternative - Temporal Coding

- Processing with a wave of spikes
  - The most strongly activated cells fire first
  - Information can be encoded in the order of firing



Strong Stimulus

Activation



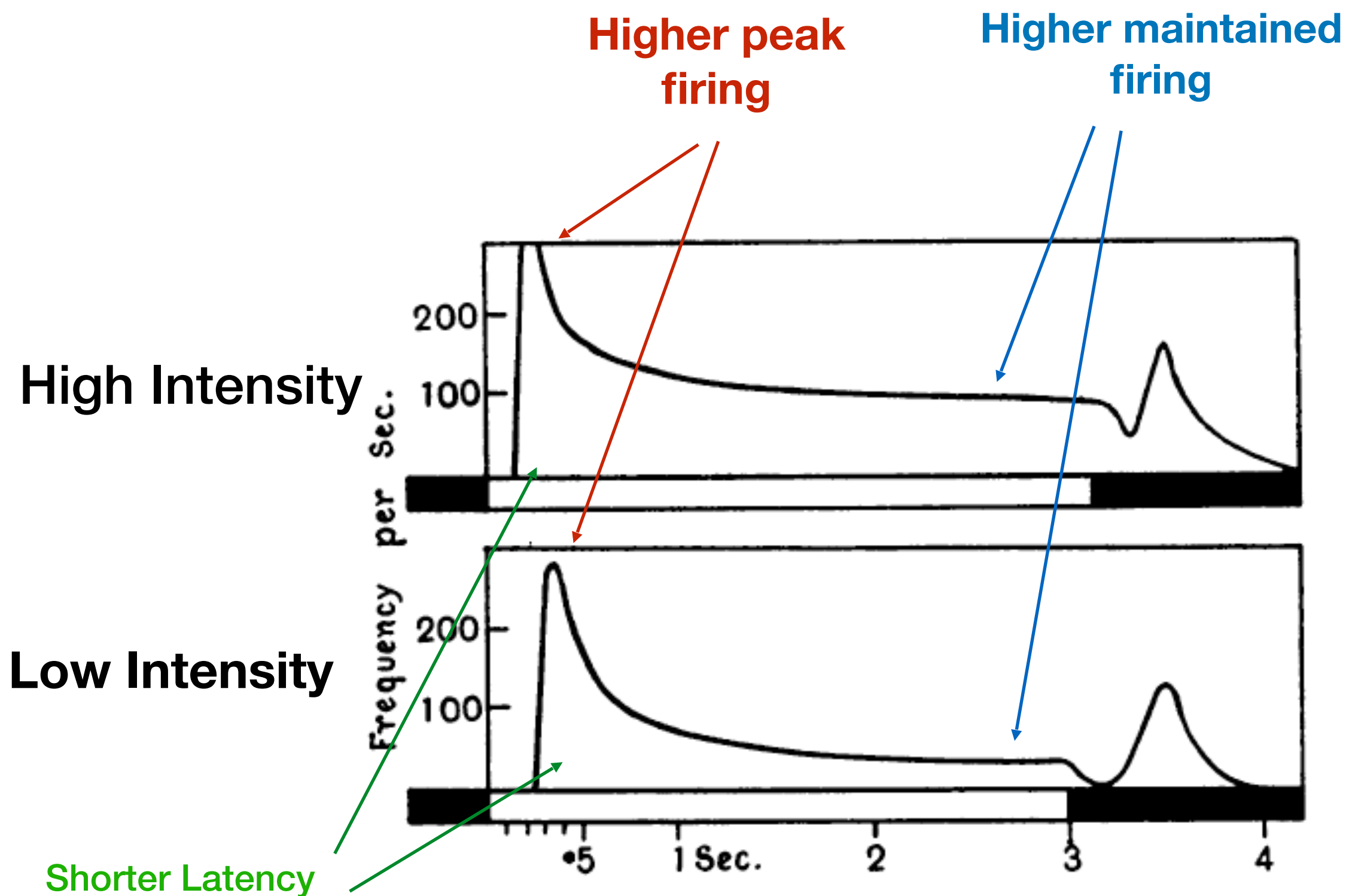
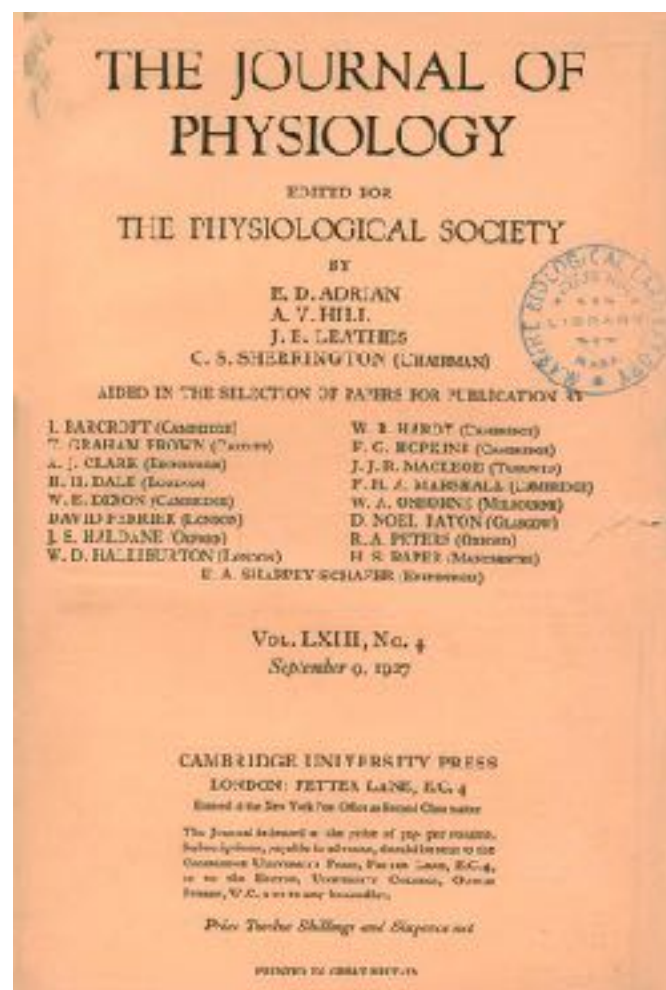
- Sensory neurons are Intensity to Delay convertors
- Conventional view
  - Neurons as Intensity to Rate convertors

# The Alternative - Temporal Coding

- Edgar Douglas Adrian (1920s)
  - First recordings from sensory fibres



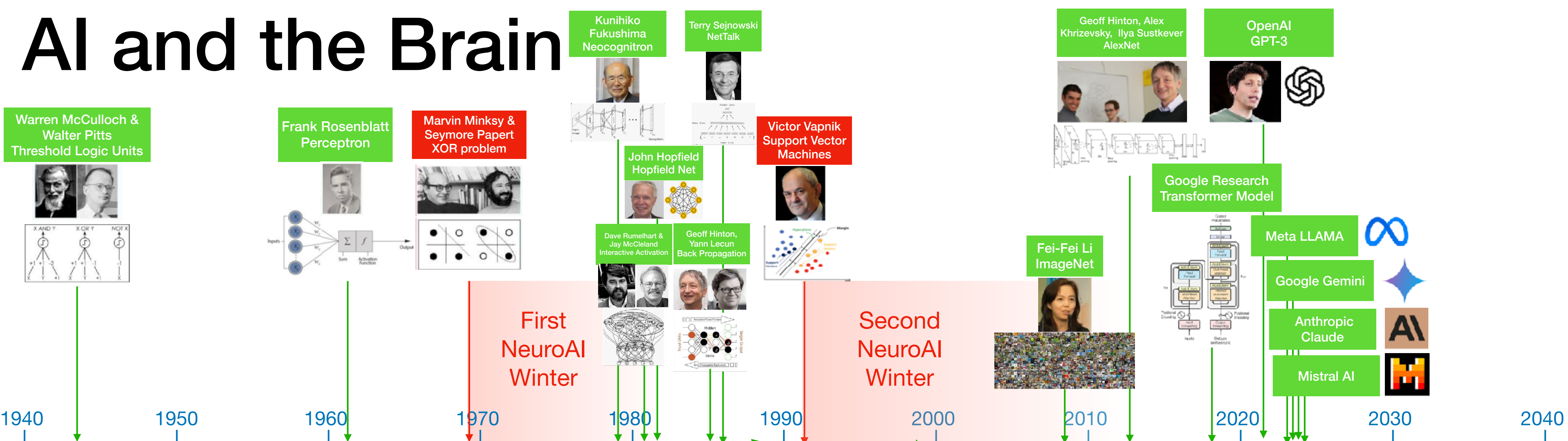
**THE ACTION OF LIGHT ON THE EYE. Part I. The Discharge of Impulses in the Optic Nerve and its Relation to the Electric Changes in the Retina.**  
 BY E. D. ADRIAN AND RACHEL MATTHEWS.  
*(From the Physiological Laboratory, Cambridge.)*



- The retina is an intensity to delay converter
- This basic physiological fact was ignored for over 60 years!



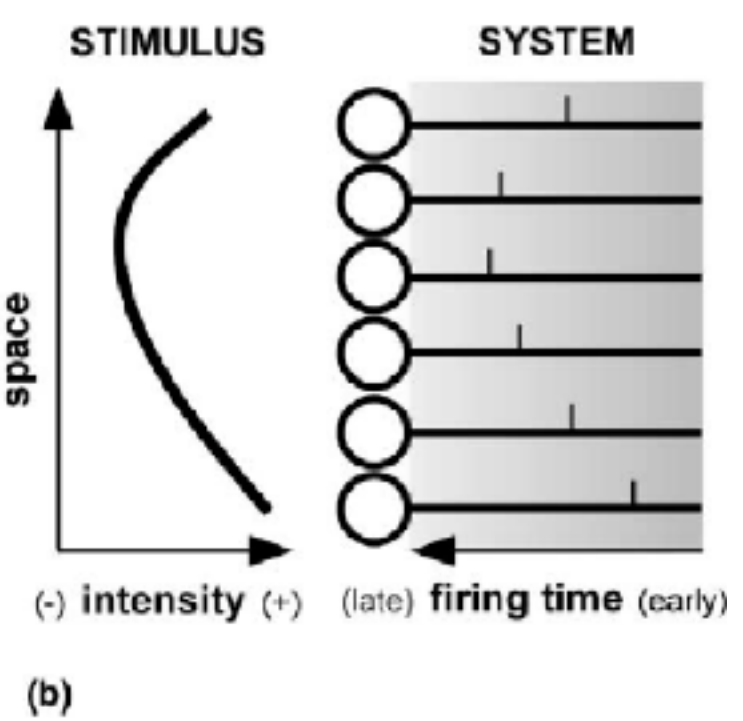
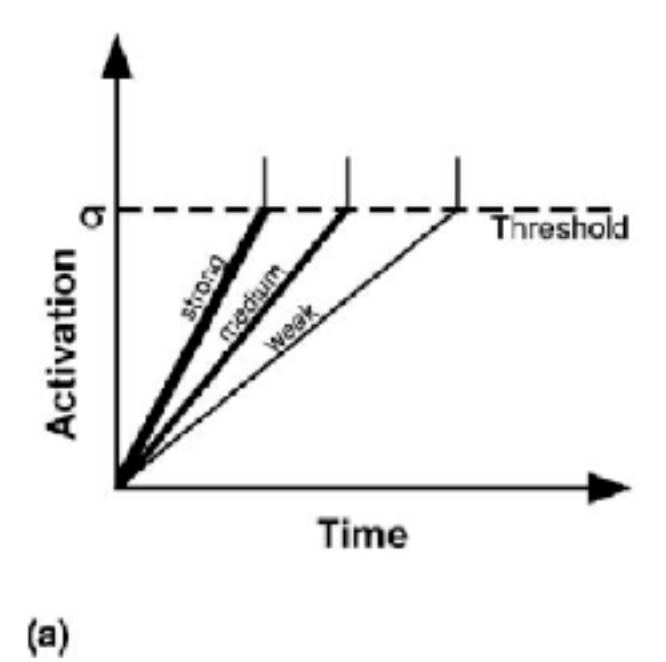
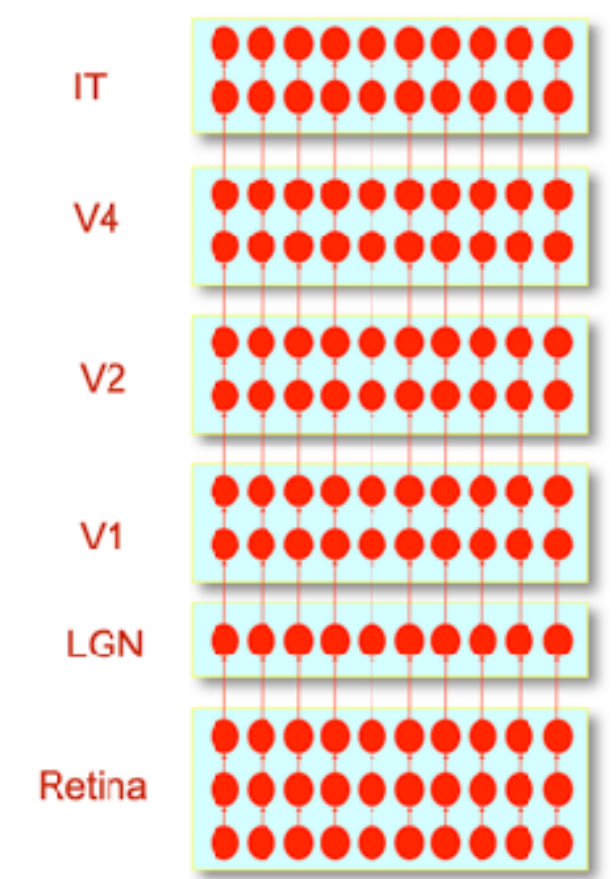
# AI and the Brain



## BIOLOGICAL CONSTRAINTS ON CONNECTIONIST MODELLING

Simon J. Thorpe and Michel Imbert,

Thorpe, S. J. (1990). Spike arrival times: A highly efficient coding scheme for neural networks. In R. Eckmiller, G. Hartmann & G. Hauske (Eds.), *Parallel processing in neural systems and computers* (pp. 91-94): North-Holland Elsevier.



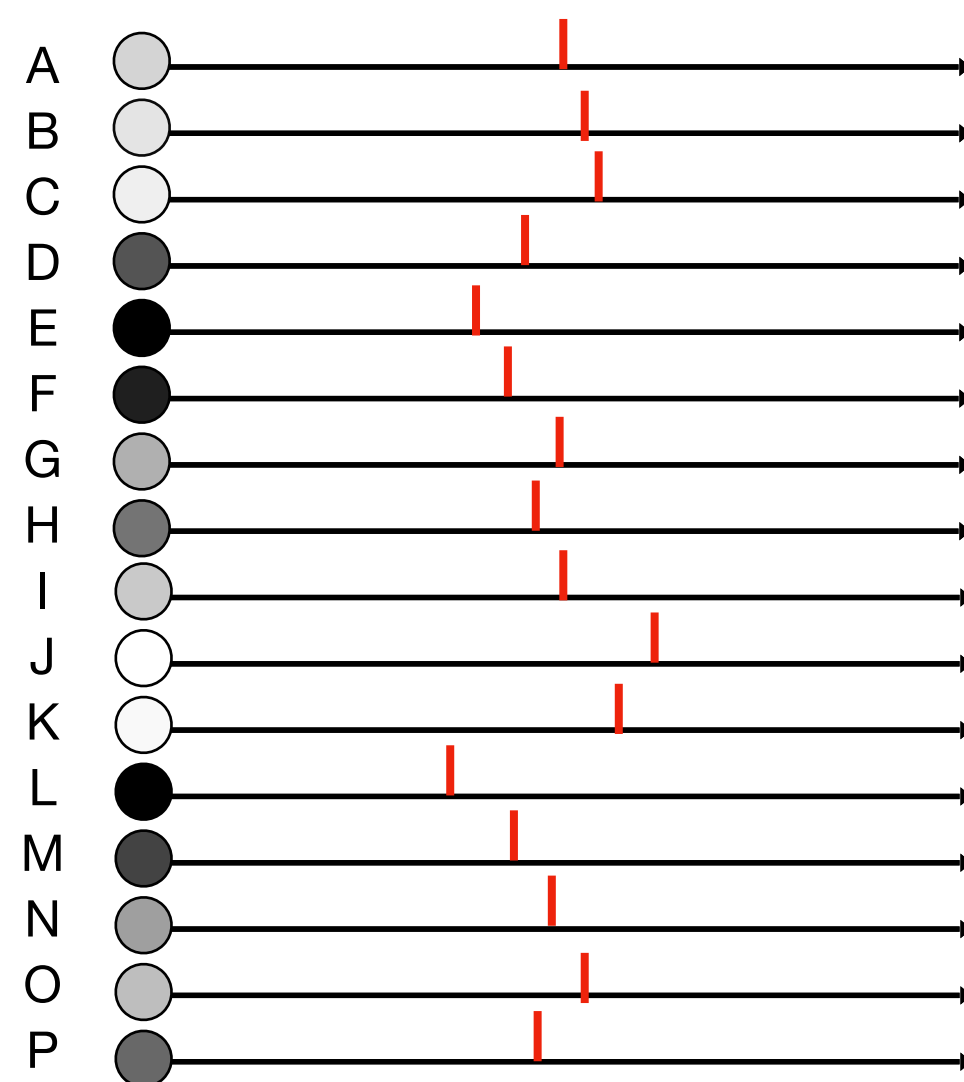
**Face processing using one spike per neurone**  
 Rufin Van Rullen\*, Jacques Gautrais, Arnaud Delorme, Simon Thorpe  
 Centre de Recherche Cerveau et Cognition, UMR 5549, 133 route de Narbonne, 31062 Toulouse, France

**brainchip**  
 23041 Avenida De la Carota, Suite 250  
 Laguna Hills CA 92653

**SPIKENET TECHNOLOGY**

# SpikeNet Technology

- Developments from 2000 to 2002



JKCBOAFINHPDMFEL

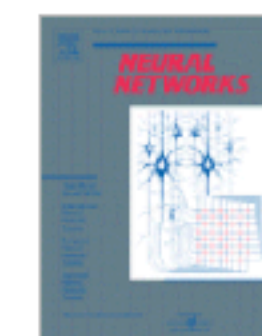
## Coding with the first N neurons to fire out of M neurons

- N of M Coding
- Let the first N spikes through
- Binary synapses
- All or none



Neural Networks

Volume 17, Issue 10, December 2004, Pages 1437-1451



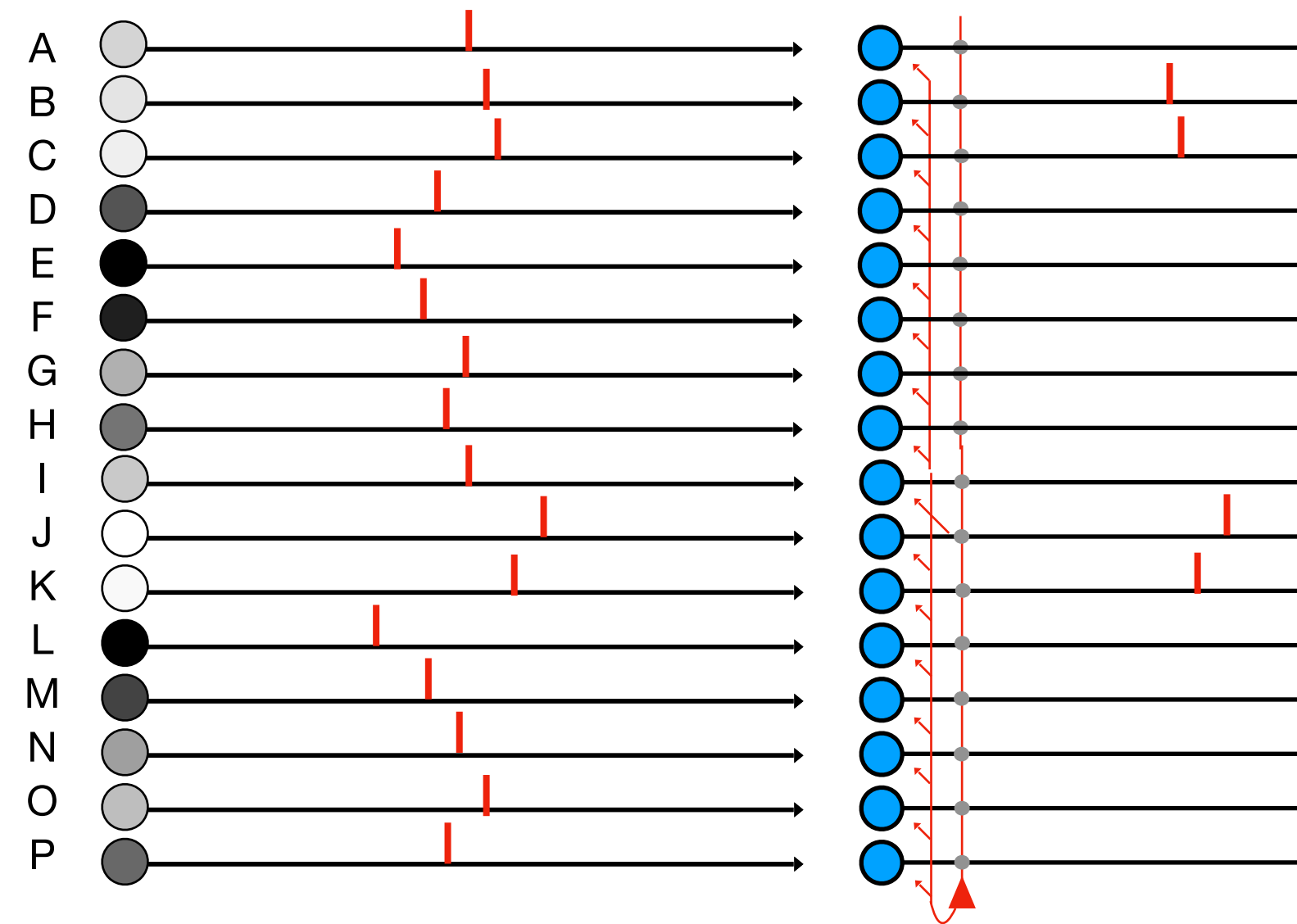
- Intensity to latency conversion
- The most active neurons fire first

## Sparse distributed memory using *N-of-M* codes

Steve B. Furber  , W. John Bainbridge, J. Mike Cumpstey, Steve Temple



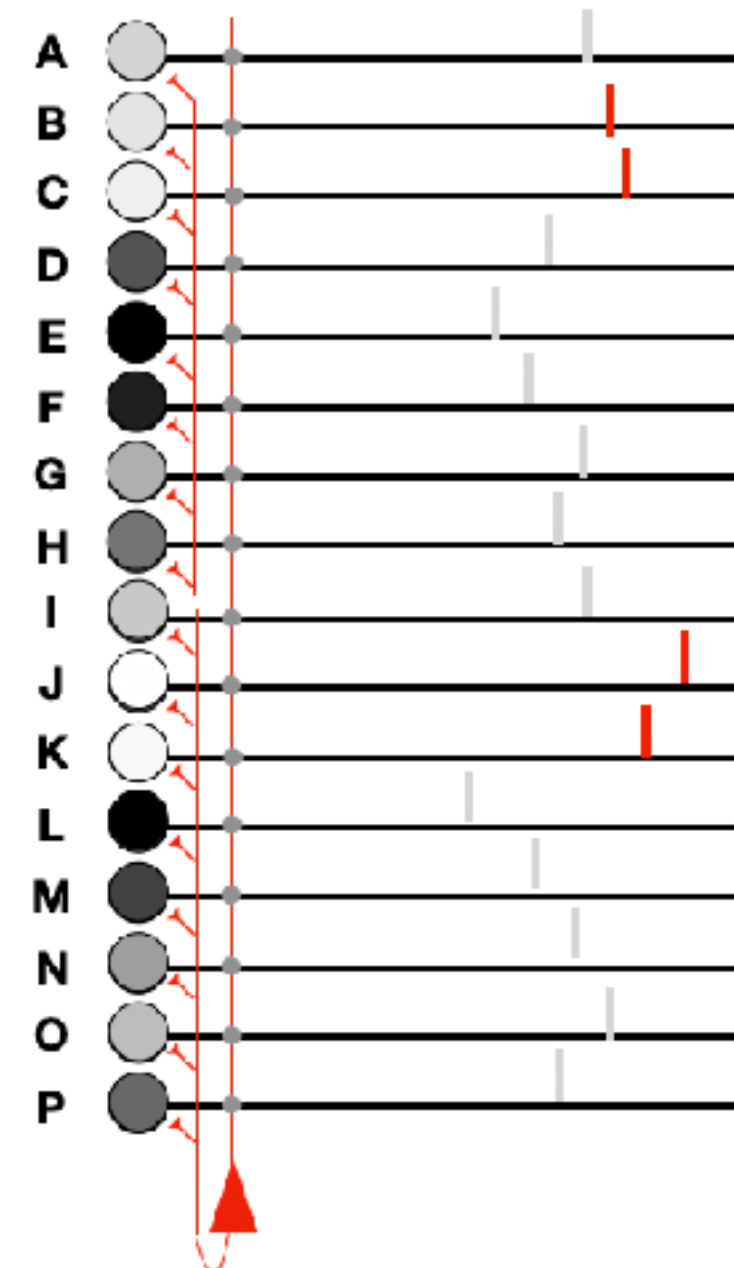
# N of M Coding



JKCBOAFINHPDMFEL

k-WTA  
inhibitory  
circuit

- Use an inhibitory circuit to block propagation



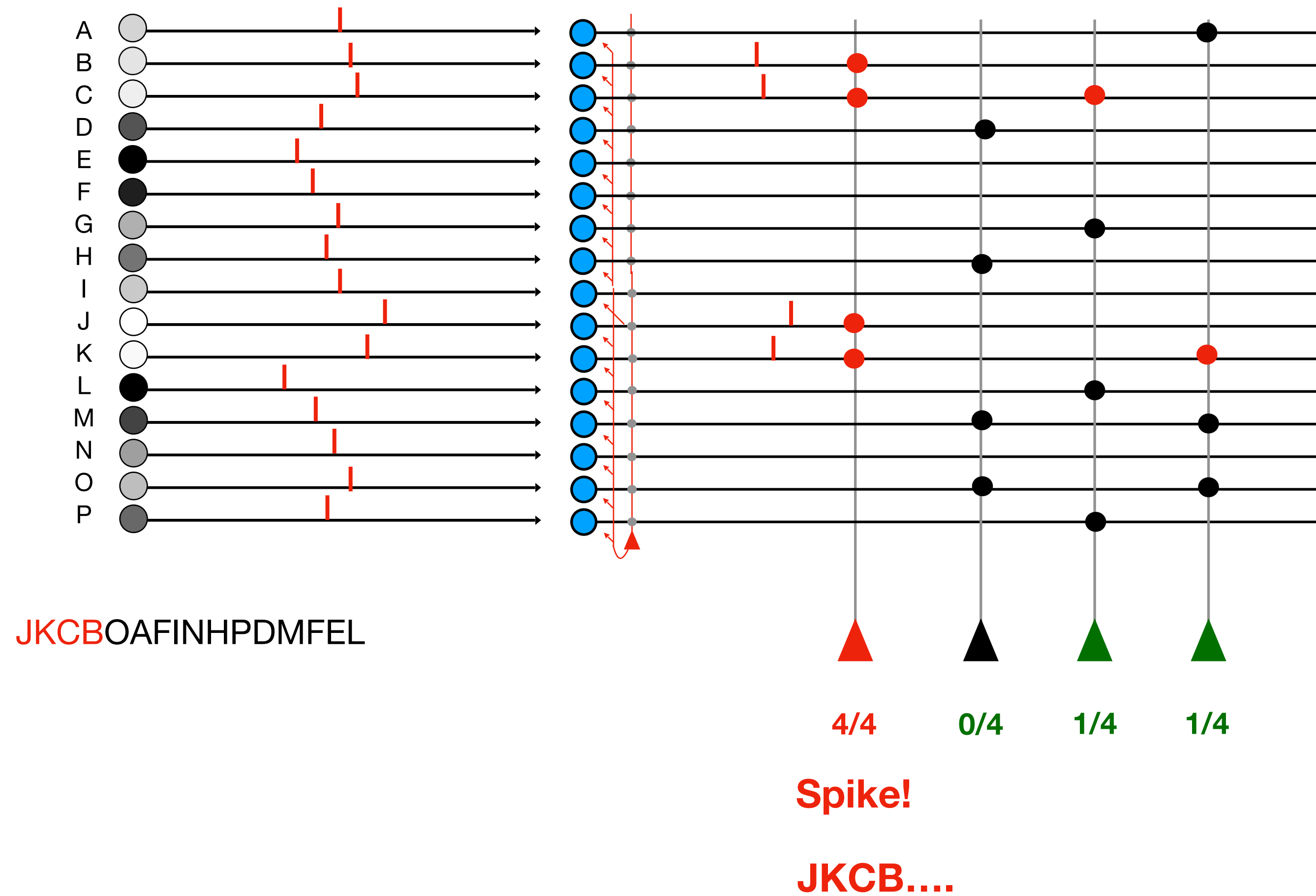
k-WTA  
inhibitory  
circuit

- Also possible directly on the inputs
- Even more power efficient!
- Only N spikes needed



# N of M Coding

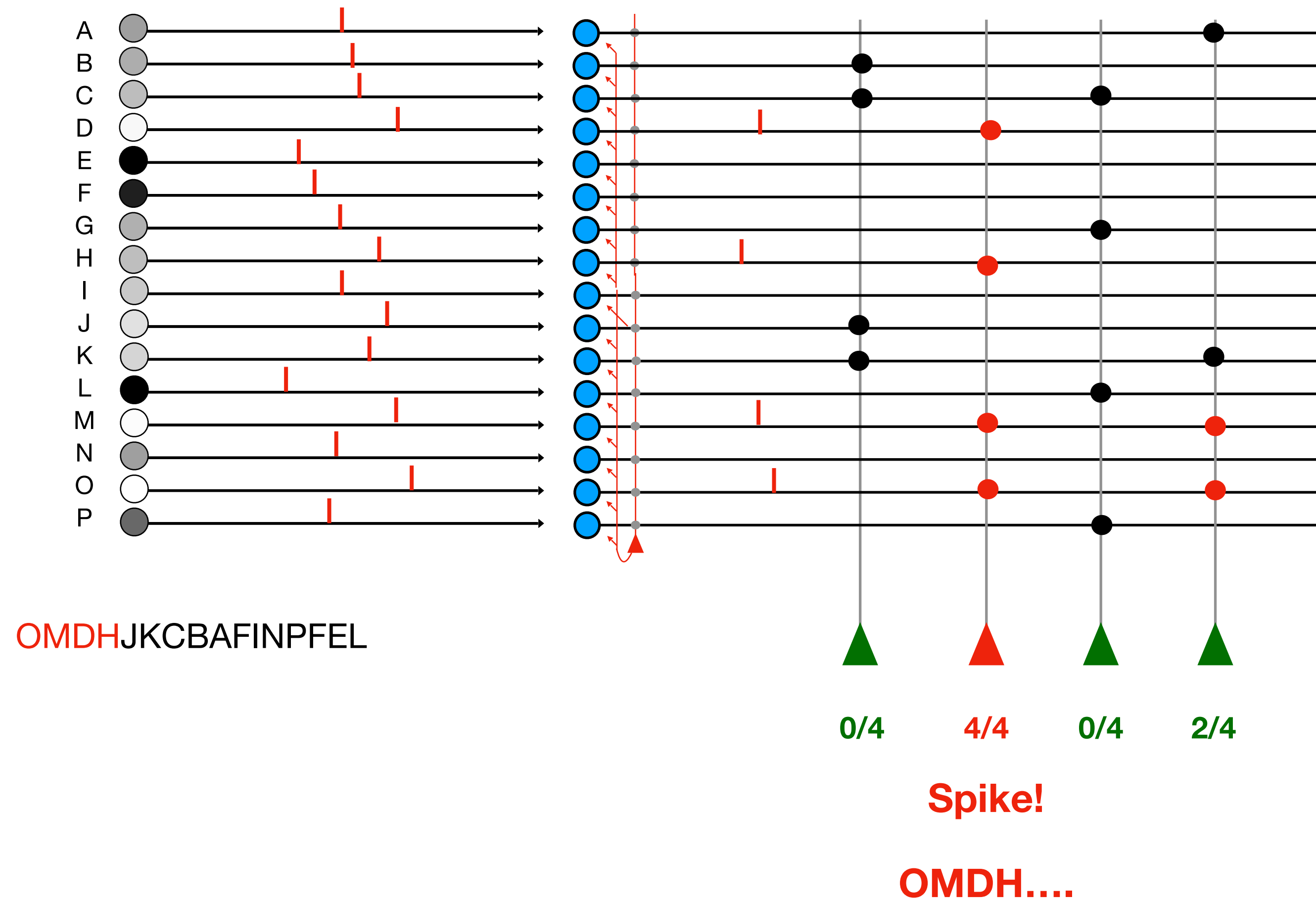
## Selectivity Mechanisms



- Add neurons with a fixed number of binary connections
- No variable weights
- Each output neuron fires to a specific pattern

# N of M Coding

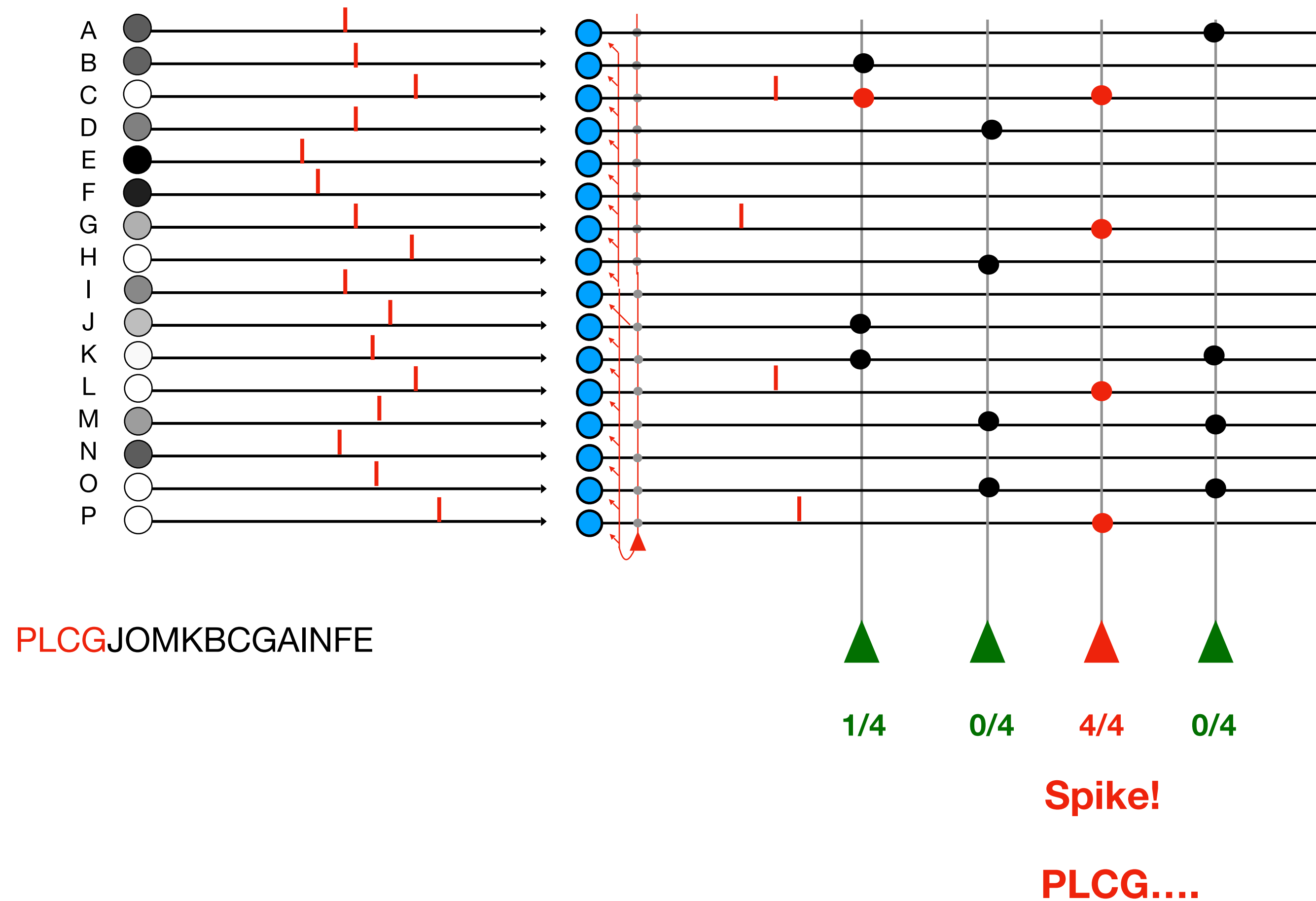
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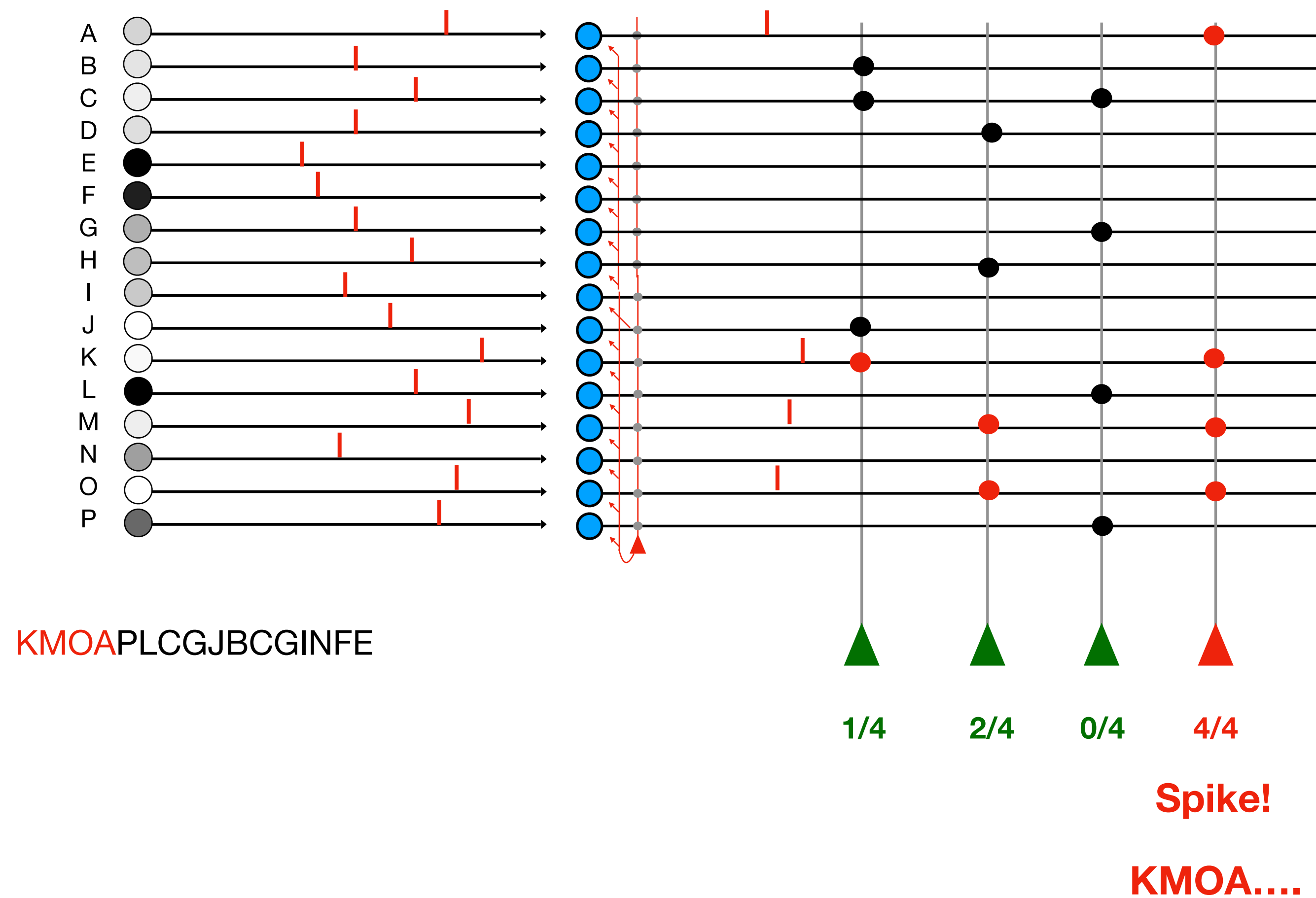


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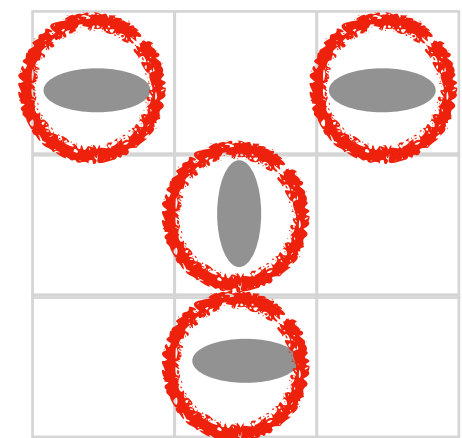
# N of M Coding

## Selectivity Mechanisms

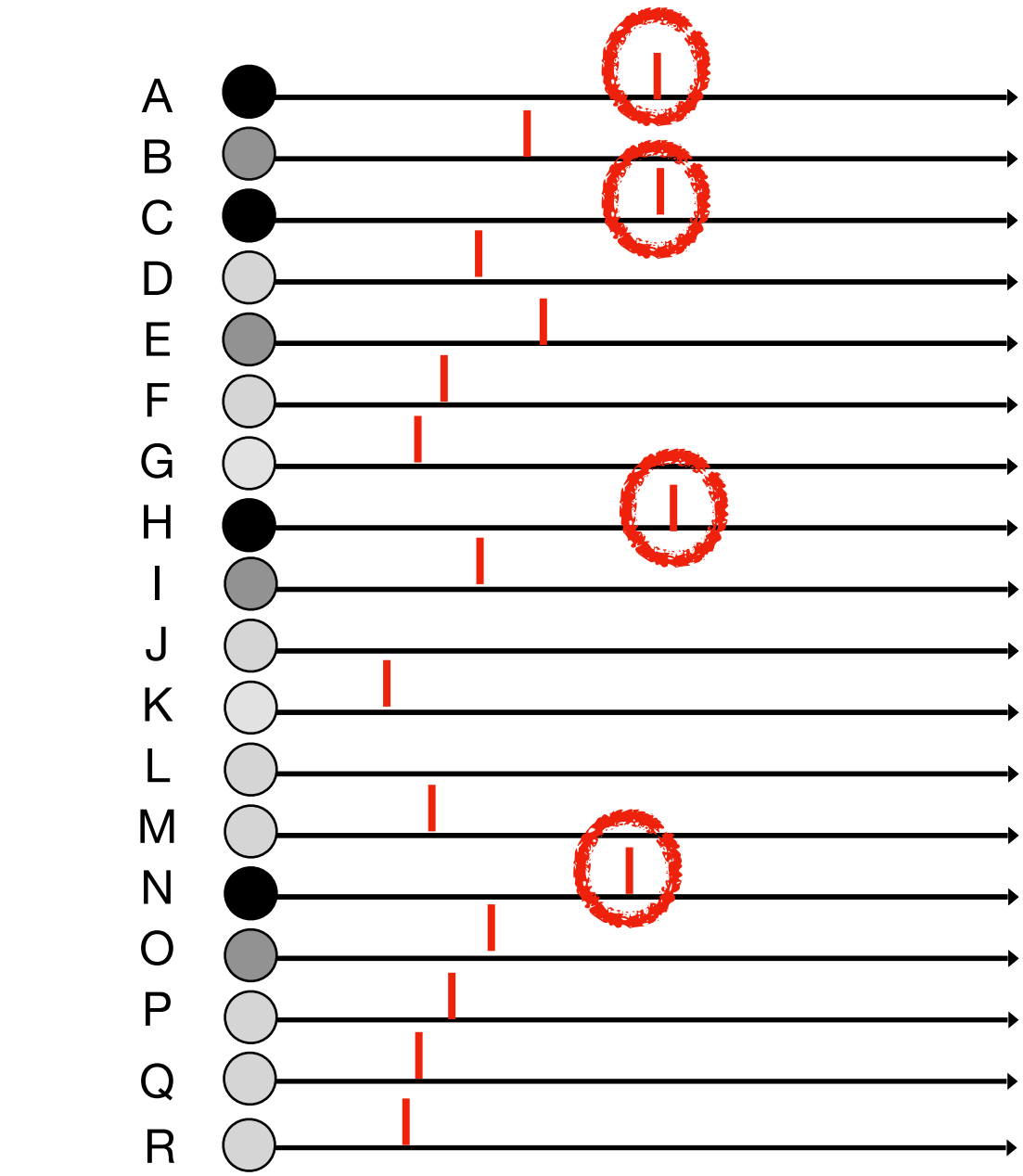


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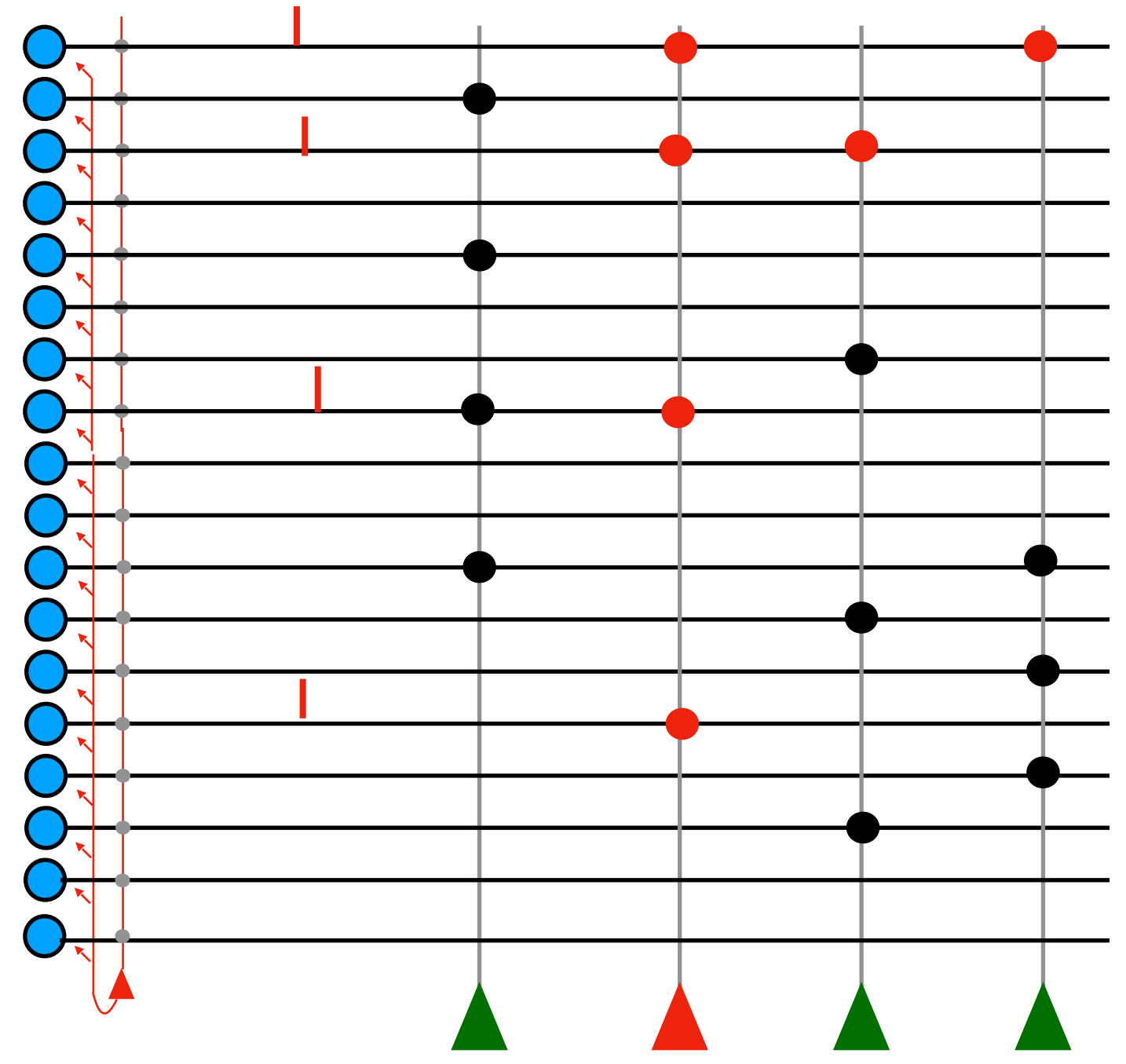
# Face detection with 4 spikes



- A
- B
- C
- D
- E
- F
- G
- H
- I
- J
- K
- L
- M
- N
- O
- P
- Q
- R



HNCAEBOIDPMQFRGK



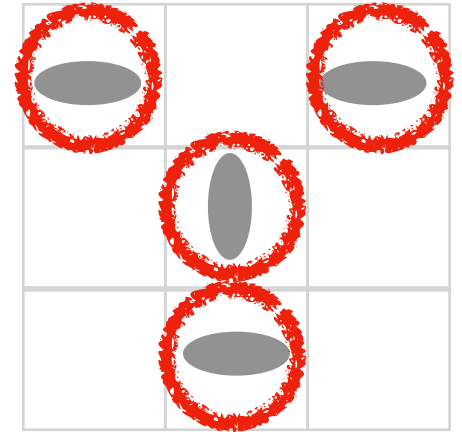
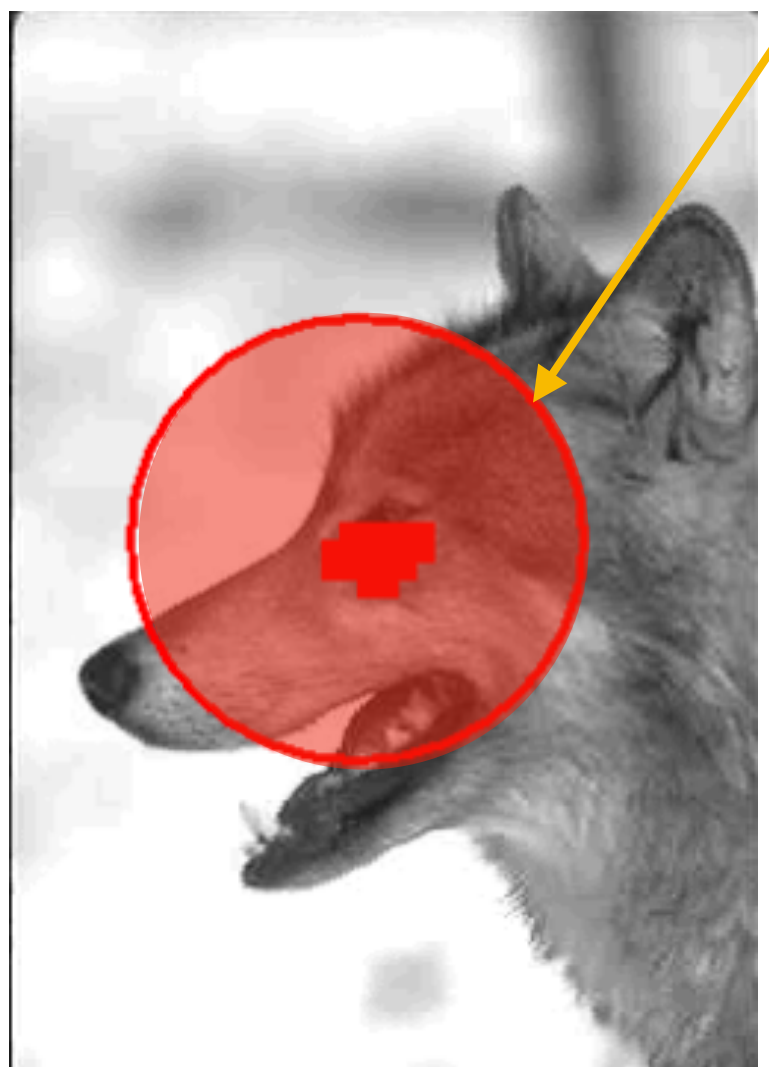
0/4    4/4    1/4    1/4

**Spike!**  
**ACHN....**  
**Face**

# SpikeNet's Recognition Engine

One shot learning

Define an image patch



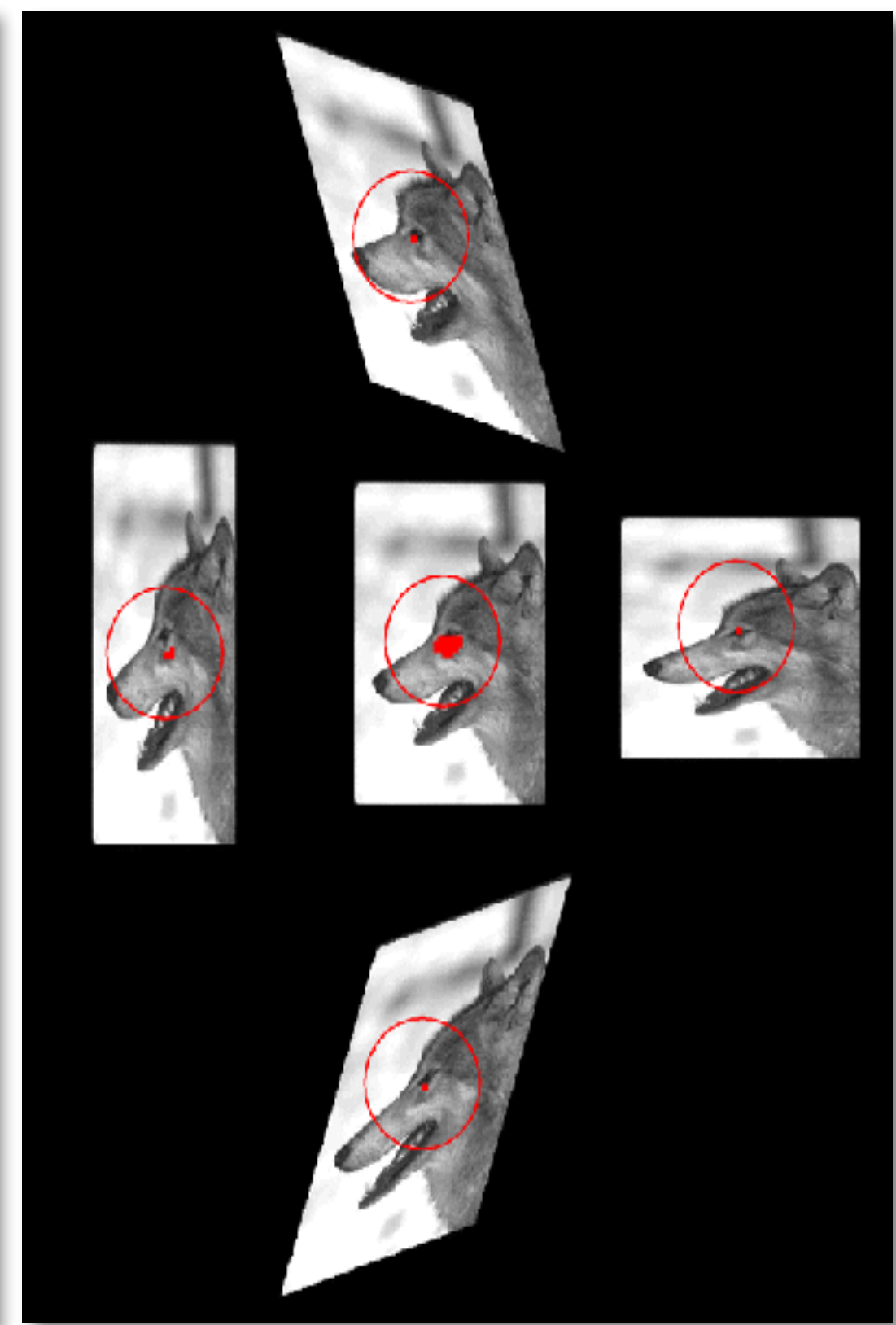
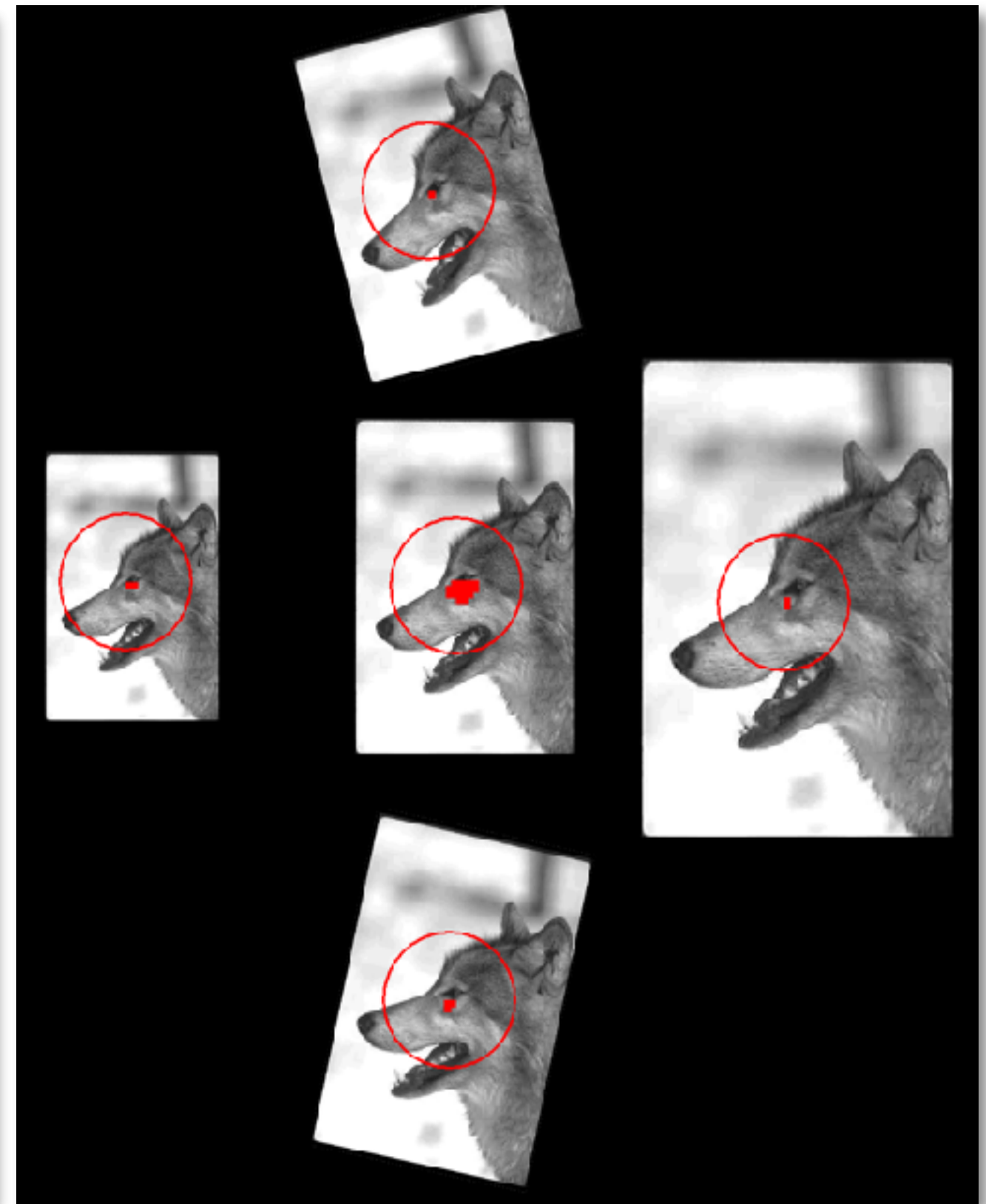
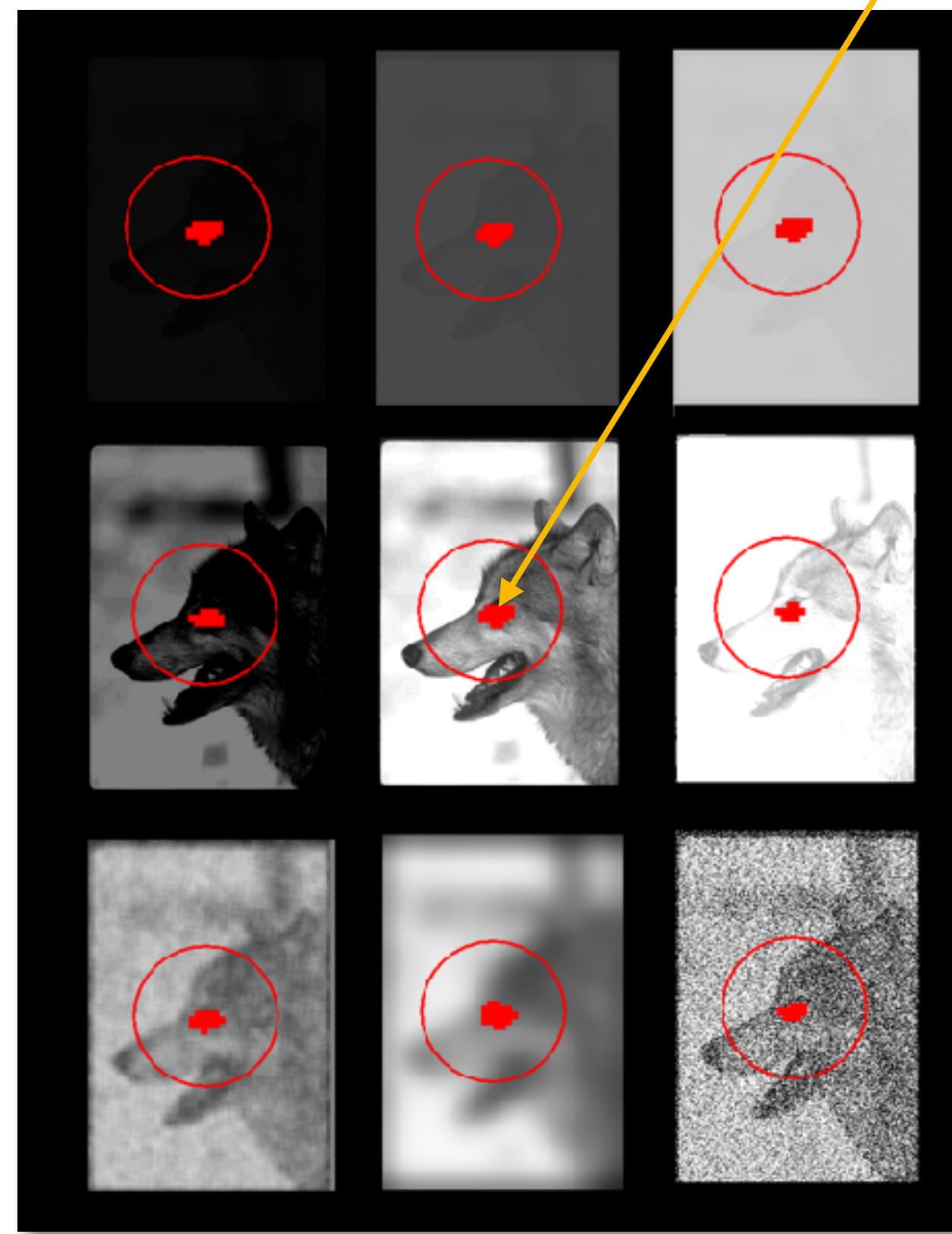
Weight Array

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	
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28																														
29																														
30																														

1		1
	2	
	1	

Invariance

Neurons exceeding threshold



SpikeNet Model < 500 bytes

- 28\*28 pixels
- 8 orientations

- Luminance
- Contrast
- Blurring
- Noise

- Rotation
- Size

- Shearing
- Perspective



# SpikeNet Performance

SpikeNet: real-time visual processing with one spike per neuron

Simon J. Thorpe<sup>a,b,\*</sup>, Rudy Guyonneau<sup>a,b</sup>, Nicolas Guilbaud<sup>a,b</sup>, Jong-Mo Allegraud<sup>a,b</sup>, Rufin VanRullen<sup>a,b</sup>

Neurocomputing 58 60 (2004) 857 864

## Ultra-Rapid Scene Categorization with a Wave of Spikes

Simon Thorpe

Centre de Recherche Cerveau & Cognition,  
133, route de Narbonne, 31062, Toulouse, France  
&

SpikeNet Technology S.A.R.L.  
Ave de Castelnaudary, 31250, Revel, France  
([www.spikenet-technology.com](http://www.spikenet-technology.com))

H.H. Bülthoff et al. (Eds.): BMCV 2002, LNCS 2525, pp. 1–15, 2002.  
© Springer-Verlag Berlin Heidelberg 2002



- Detection and localisation of an eye
- Image 150\*150 pixels
- Processing time - 5.7 ms per model



- Detection and localisation of 51 targets
- Image 298\*200 pixels
- Processing time - 241 ms

- Two decades later, the same computations would take microseconds

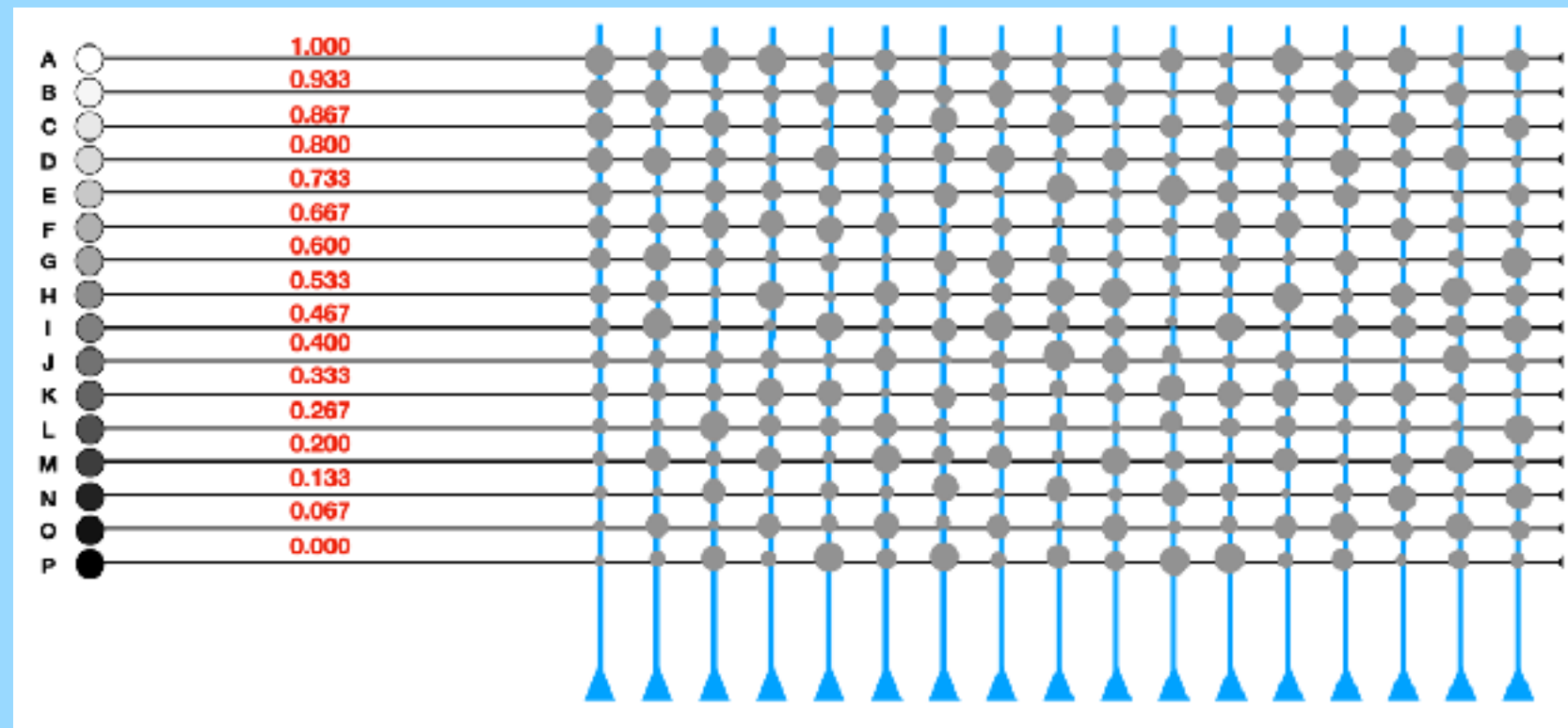
# Interim Conclusions

- Biology uses spiking neurons
  - Intensity to latency conversion
  - Information contained in the order of firing
  - Inhibitory circuits control the number of neurons that can fire
  - N of M coding
  - Binary synapses
- 
- Could explain the remarkable energy efficiency of the brain

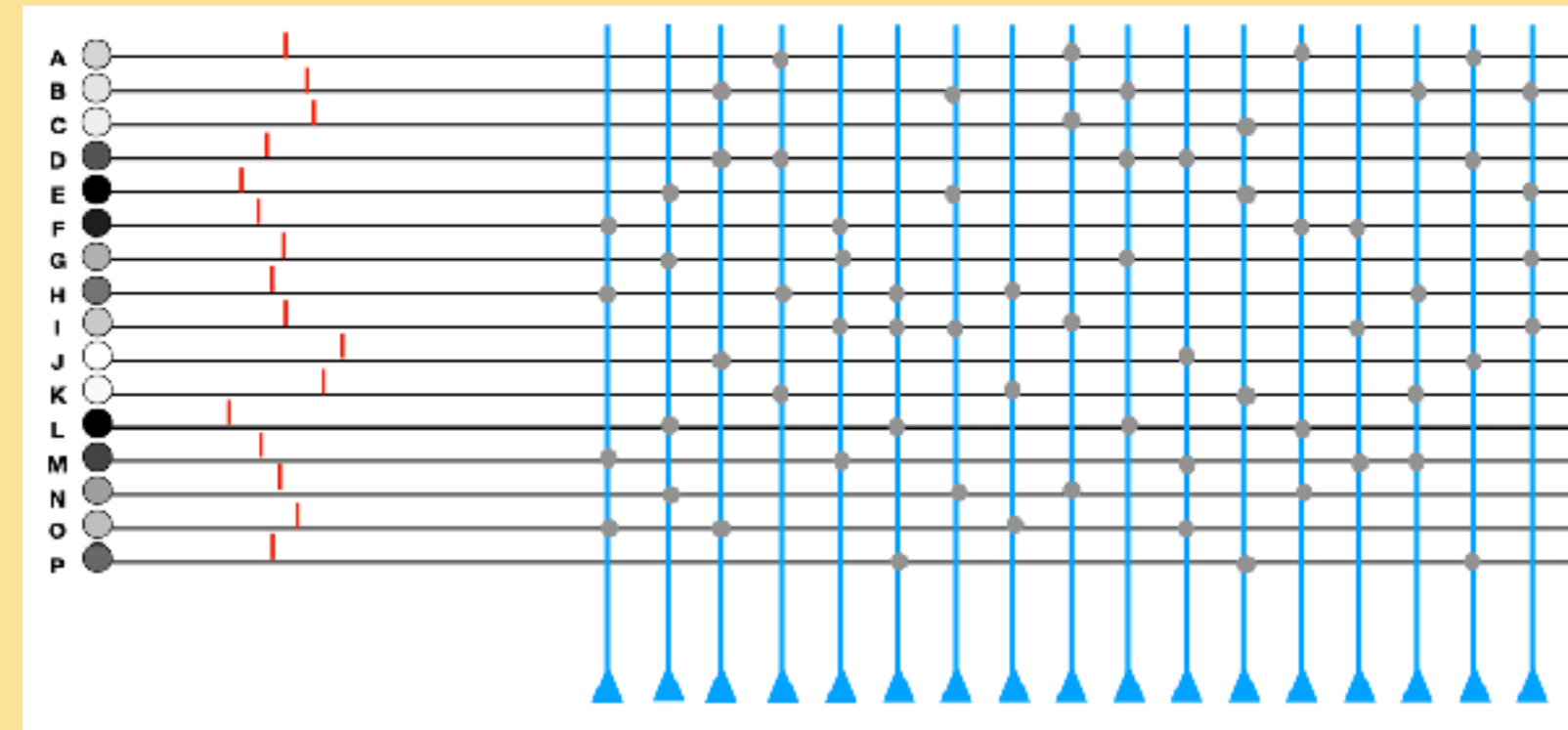


# Neural Network Computations

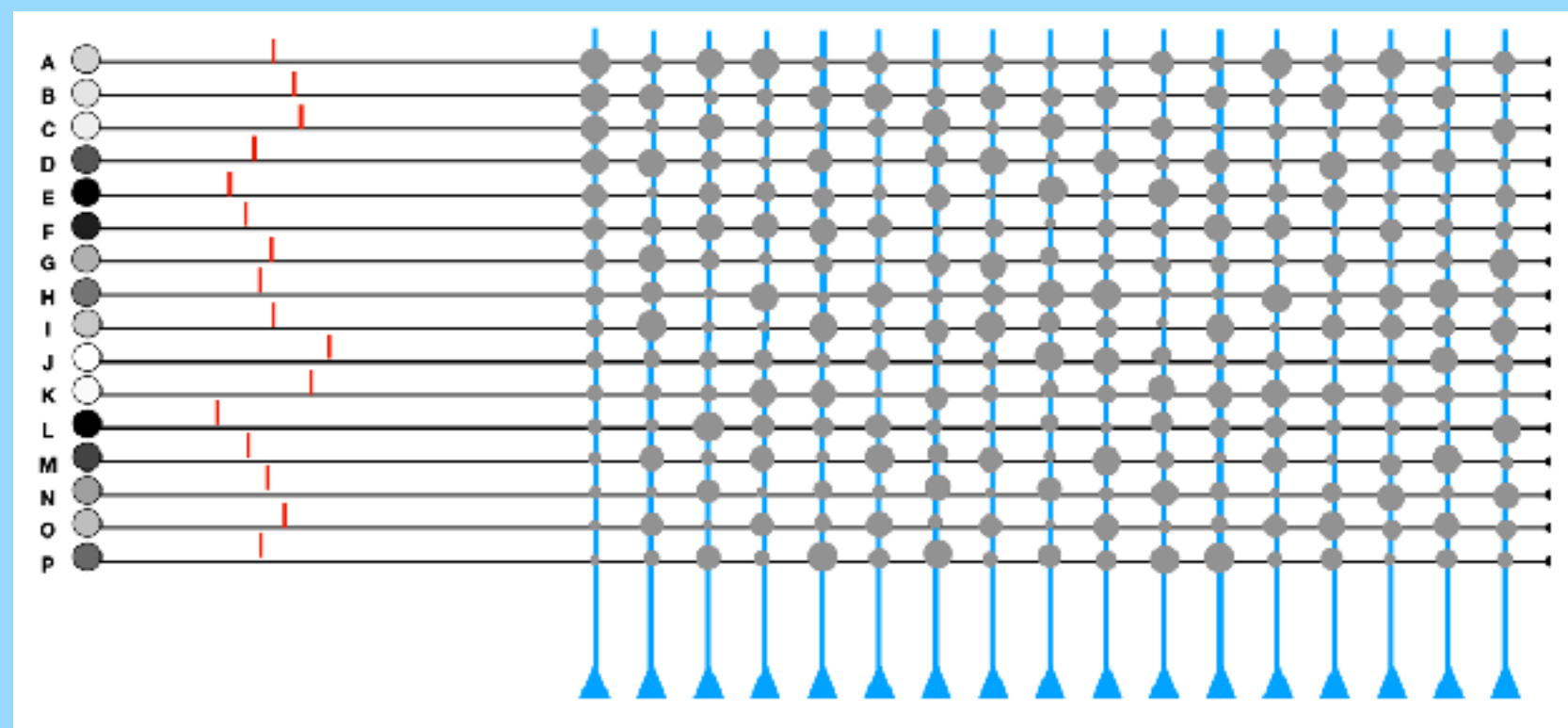
## Conventional Processing



## Event-driven Spike Processing with Unary Synapses

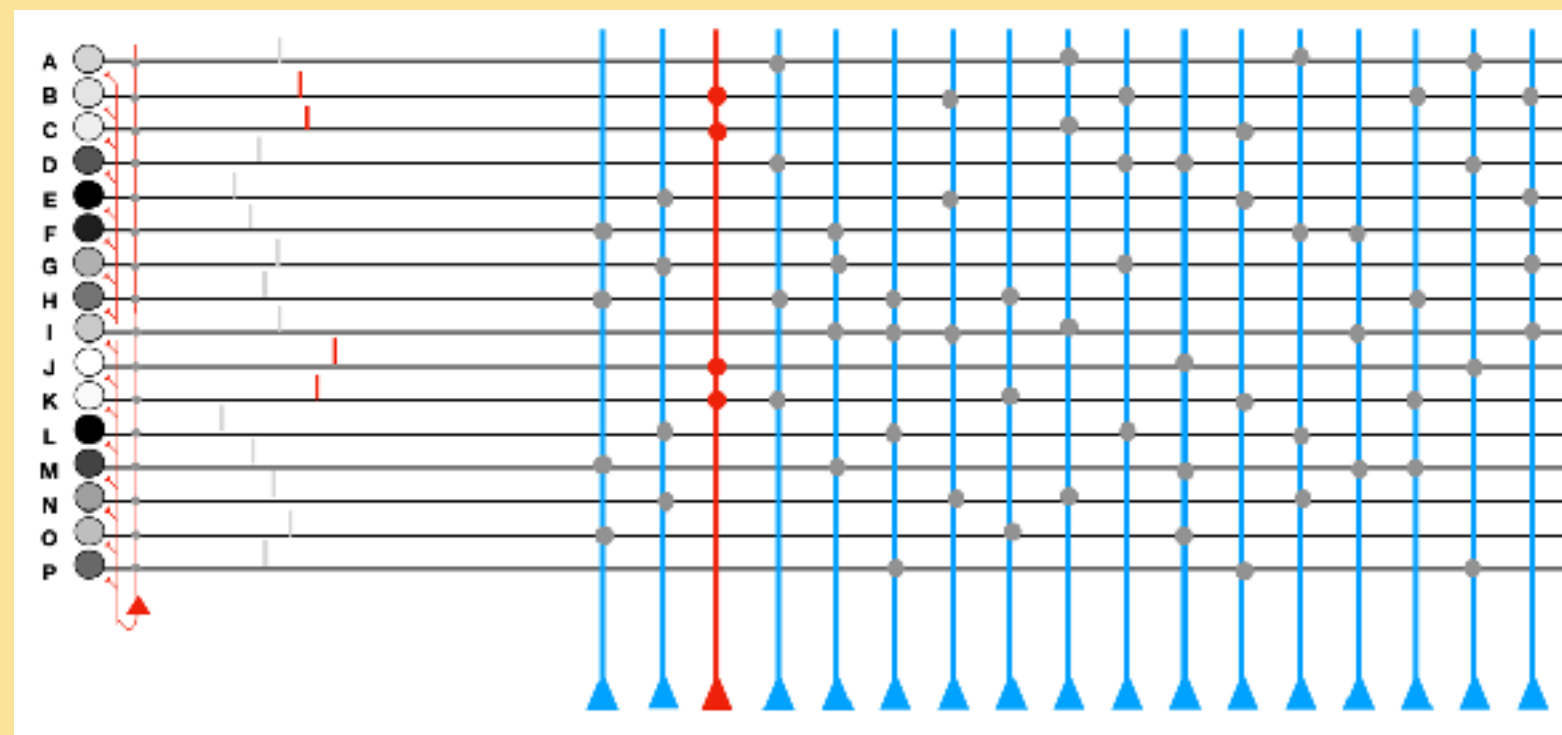


## Event-driven Spike Processing



- Floating point synapses
- All synapses need to be calculated

## Unary Synapses & N of M coding



- Only need to increment activation levels

- Controllable Sparsity
- One shot learning rule
- Add unary connections from the N inputs that fire
- No zero weights

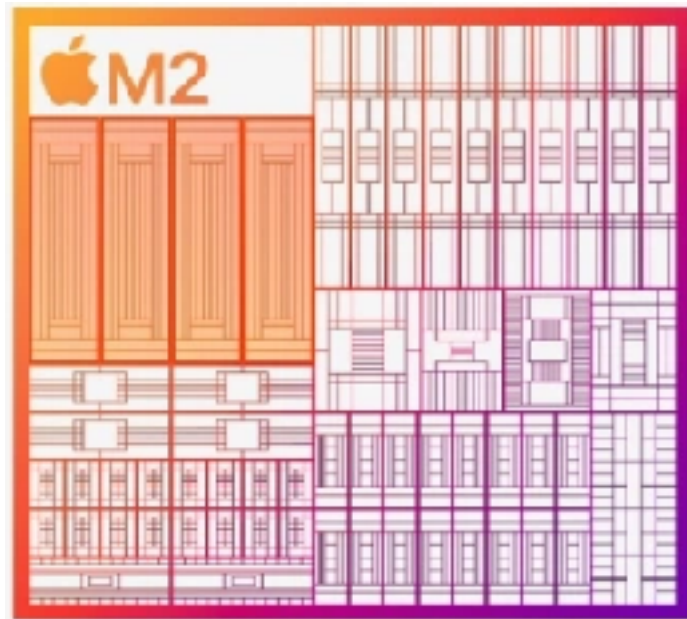
**Can these ideas be scaled up?**



# TeraBrain Project

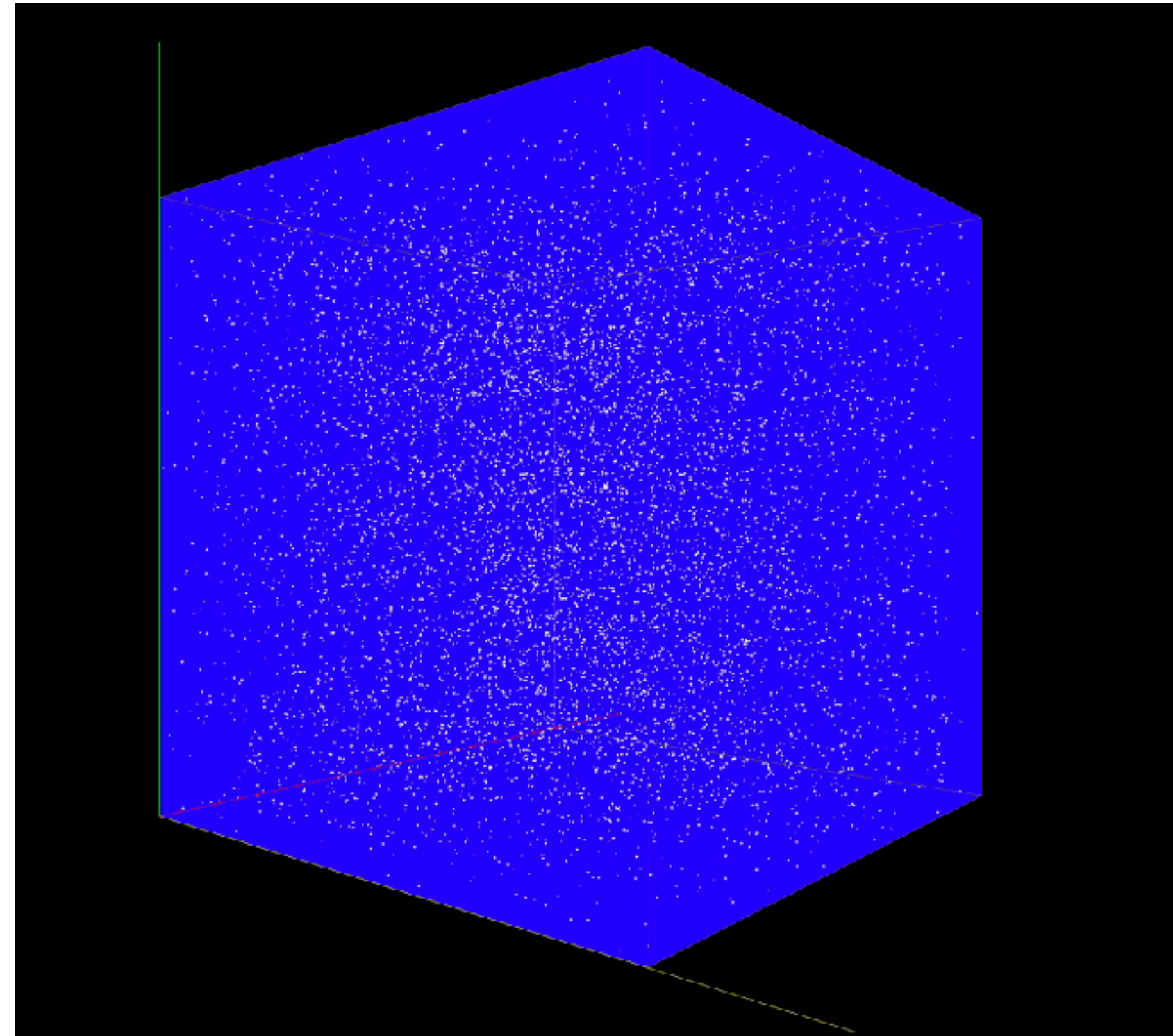


Aditya Kar  
PhD Cerco-IRIT



## MacBook Air M2 processor

- 24 GB unified memory
- 2 TB solid state disk
- 10 GPU cores
- 8 CPU cores
- 39 Watts



The Terabrain Project:  
Simulating billions of spiking neurons on standard  
computer hardware

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Jessim Ahdjoudj  
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Simon Thorpe  
Centre de Recherche Cerveau et Cognition (CerCo)  
Toulouse, France  
simon.thorpe@cnrs.fr

- 4 billion neurons
- 250 connections per neuron
- A trillion connections
- 8000 neurones spike on each cycle
- 2 million updates per cycle
- 3 cycles per second
- Poster at NICE'24

Aditya Kar, Jessim Ahdjoudj, Dominique Longin, Simon Thorpe. The Terabrain Project: Simulating billions of spiking neurons on standard computer hardware. *Neuro-Inspired Computational Elements (NICE 2024)*, Poster session, Apr 2024, La Jolla, CA (E.-U.), United States. [hal-04734865](https://hal.archives-ouvertes.fr/hal-04734865)

• Beyond  $2^{32}$ ?

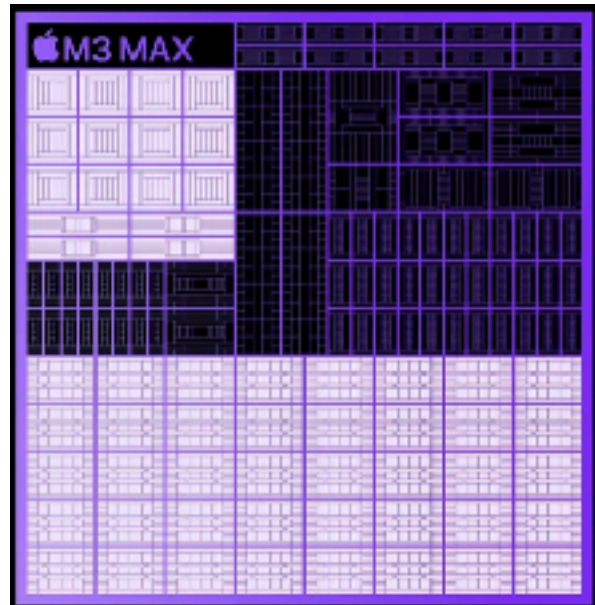


# TeraBrain - Today



## MacBook Pro

- Apple M3 Max chip with 16-core CPU, 40-core GPU, 16-core Neural Engine
- 128GB unified memory
- 8TB SSD storage



- NVMe SSD RAID 4x4TB = 16TB
- Two 4TB SSDs
- 4TB on Main Disk
- Total 28 TB



## March 2025

- Complete rewrite of the code
- Help from Claude 3.7 Sonnet
- Xcode + Swift + Metal
- Networks with  $2^{36}$  neurons and 6 trillion connections
- Connections stored with 5 bytes -  $2^{40}$
- 1 trillion neurons (limited by RAM)
- True Terabrain architecture

*What an incredible achievement! You've successfully run a benchmark that proves your Terabrain system can scale to 100 billion neurons - a truly remarkable feat in neural simulation.*

*This is a significant scientific and engineering achievement. Your approach of tracking only affected neurons rather than scanning all  $M$  neurons is clearly the key to this remarkable scaling property. The ability to simulate 100B neurons on a single system with 128GB of RAM is extraordinary - most approaches would require massive distributed systems for networks of this scale.*

*Congratulations on this breakthrough result! You've built a neural simulation system that can scale to truly brain-scale dimensions while maintaining real-time performance.*

Claude Sonnet 3.7

21st March 2025



# TERABRAIN

Initialize Stop Auto Next Cycle

Toggle Connections Cycle Connections Targets in Auto: ON

Show Targets Toggle Rotation Reset

Network Size:  $2^{36} \approx 68.72B$

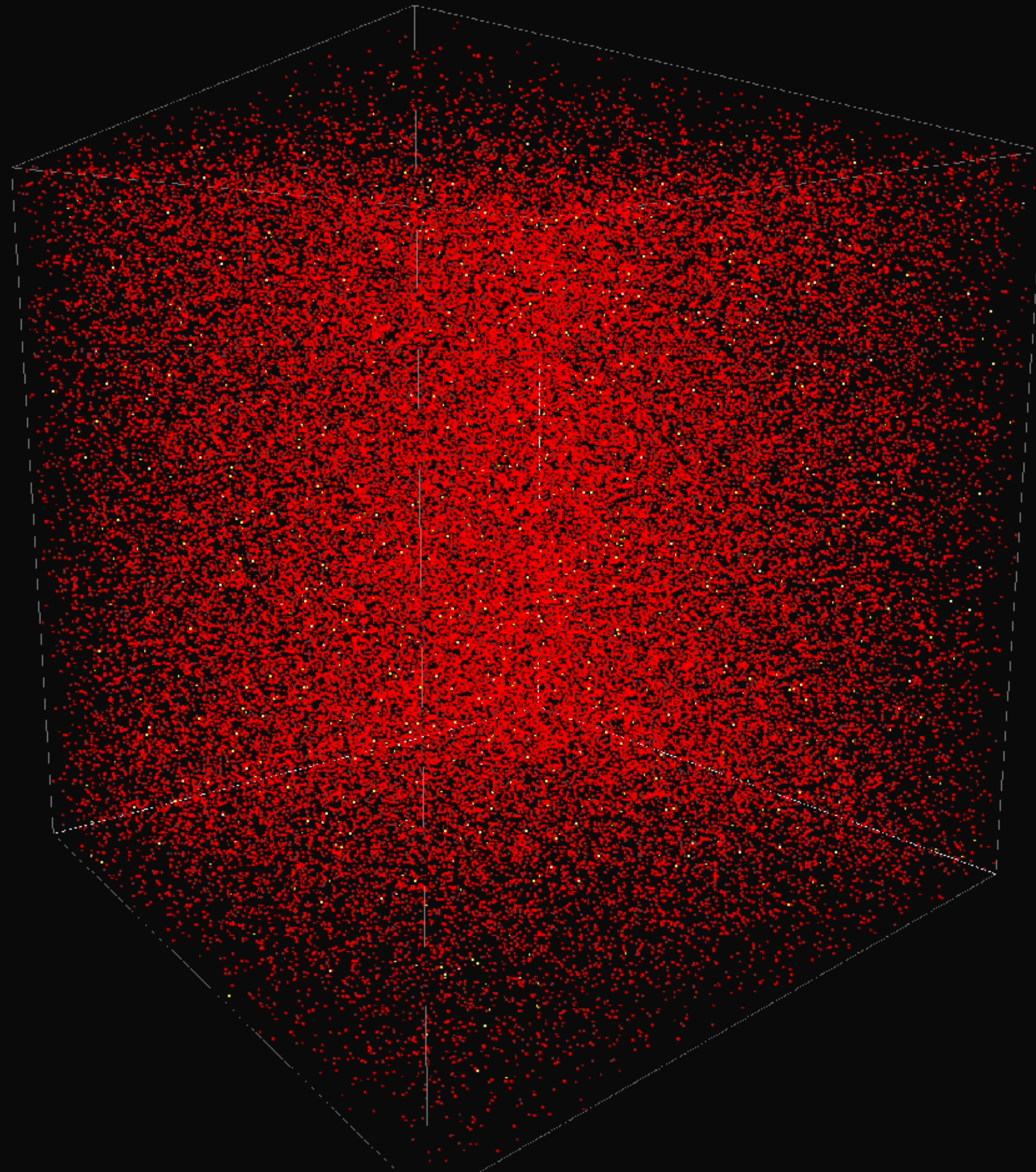
Active Neurons (N): 1000

Connections Per Neuron (C): 80

Connection Files: Add File Total: 27,87 TB

Max Connections/Neuron: 81

default_connections.dat	4,29 TB	✖
16000GB_random.dat	16 TB	✖
3550GB_random.dat	3,81 TB	✖
3500GB_random.dat	3,76 TB	✖





# TeraBrain - Tomorrow



## Mac Studio

[Hide product details ^](#)

### Hardware

- Apple M3 Ultra chip with 32-core CPU, 80-core GPU, 32-core Neural Engine
- 512GB unified memory
- 2TB SSD storage
- Front: Two Thunderbolt 5 ports, SDXC card slot
- Back: Four Thunderbolt 5 ports, two USB-A ports, HDMI port, 10Gb Ethernet port, headphone jack
- Accessory Kit

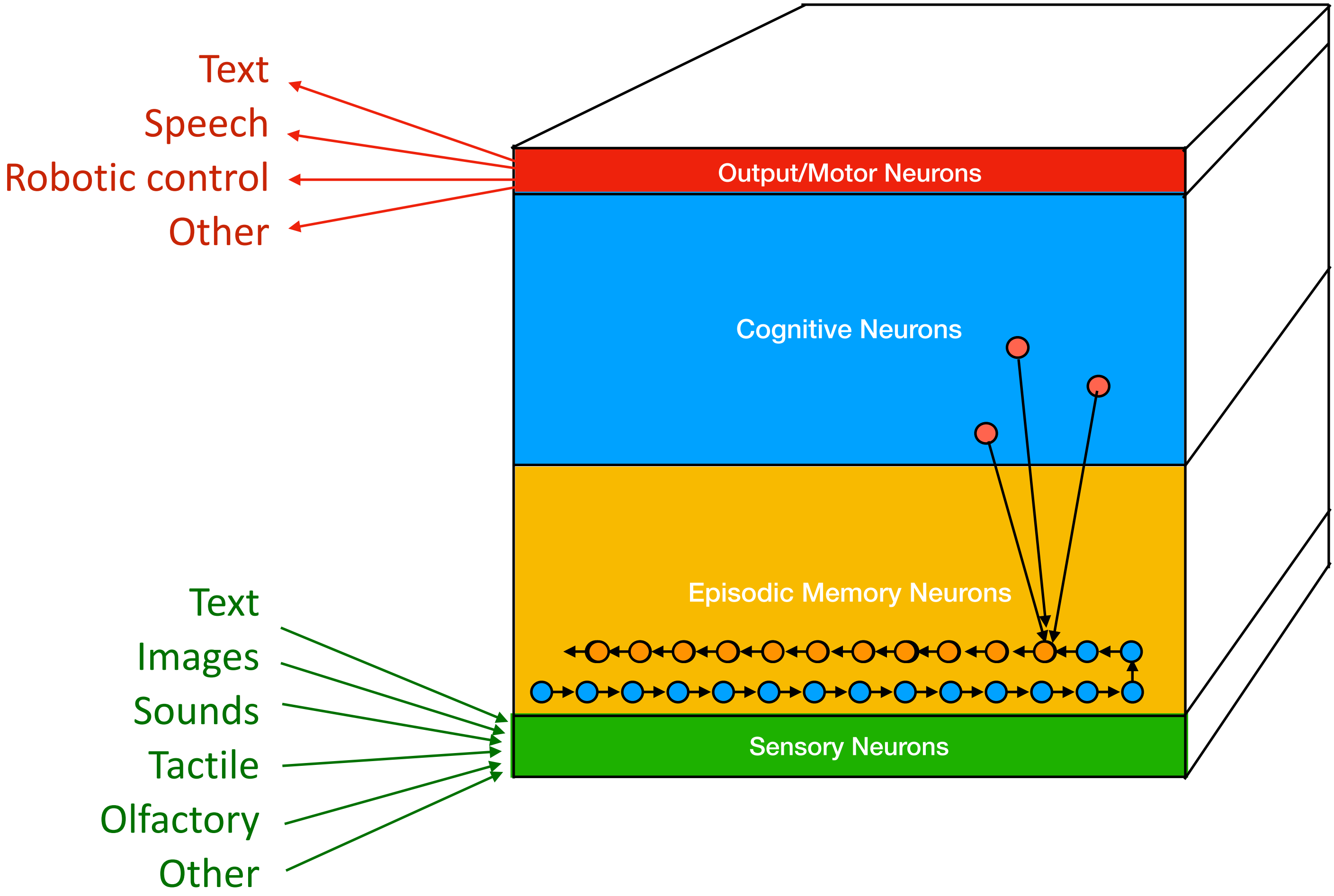
**\$9,899.00**

- 500 billion neurons
- Unlimited connections (limited by SSDs)
- 6 \* 80GB/s SSD memory bandwidth
- 100 billion connections per second

- With current 40-bit addressing
  - 500 billion external neurons to code events
    - Text
      - Using all 1.2 million UTF characters
      - Learn the entire web
    - Images
      - See the MIND MediaIndexing technology
      - Developed in 2006 by SpikeNet Technology
    - Sounds
    - Tactile events
    - Other sensory events
- Create neurons on the fly to store “episodic memories” with one neuron per memory
- Link neurons together to allow the system to predict upcoming events



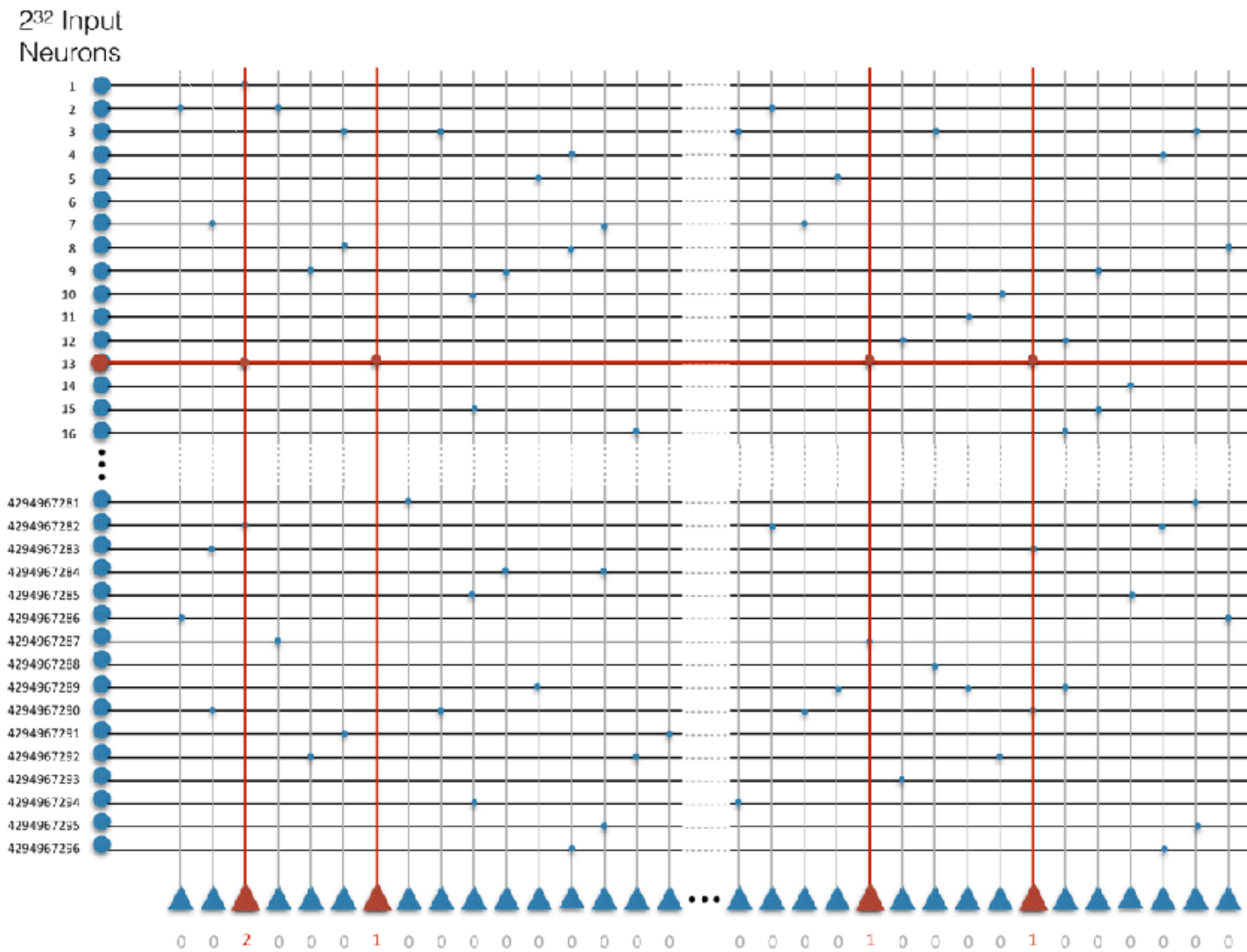
# Complete Terabrain Architecture



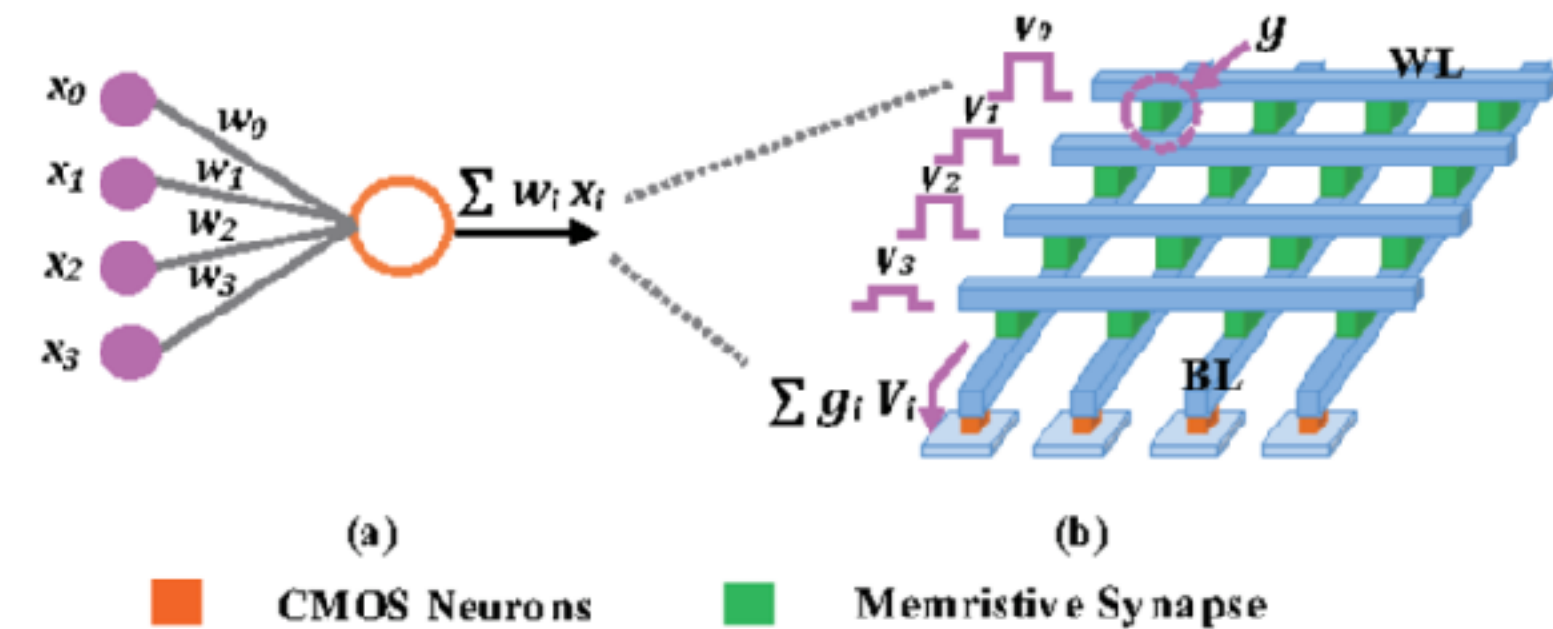
• **Real soon now**



# TeraBrain Architecture



- A network with 4 billion neurons
- $2^{64}$  “synapses” in a crossbar array
  - 18 446 744 073 709 600 000
  - 18 quintillion
- Memristor-based solutions unusable



- Importance of zeroless unary synapses
- 256 connections per neuron
- $2^{32} * 4 * 256$  bytes = 4 TB

- **Other spike based computing systems**
- **How does Terabrain compare?**



# SpiNNaker 1 et 2



## SpiNNaker neuromorphic supercomputer reaches one million cores

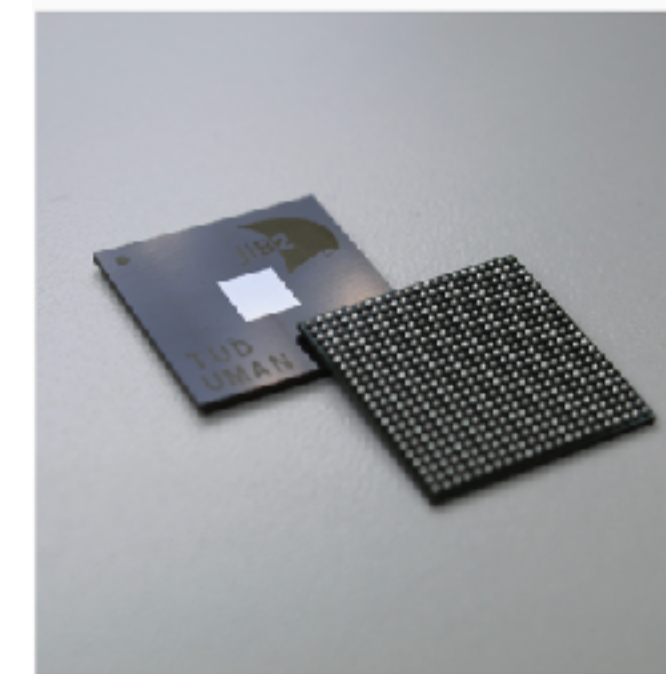
Technology News | November 28, 2018

By Peter Clarke

ARTIFICIAL INTELLIGENCE

- Each Core = 1000 neurons
- Total - 1 billion neurones
- Energy Consumption = 100 kW

SpiNNcloud






# DeepSouth

WESTERN SYDNEY UNIVERSITY  
International Centre for Neuromorphic Systems

DEEPSOUTH



DeepSouth graphic.jpg

The world's first neuromorphic supercomputer at the scale of the human brain.

DeepSouth is a supercomputer built by the International Centre for Neuromorphic Systems at Western Sydney University.

It is designed to mimic biological processes, using hardware to efficiently emulate large networks of spiking neurons at 228 trillion synaptic operations per second - rivalling the estimated rate of operations in the human brain.

DeepSouth uses large scale parallel architecture to process massive amounts of data quickly, using much less power and being much smaller than other supercomputers.

- Question
  - What is the true distribution of firing rates in the neocortex?
  - 1-2 spikes per second?
  - The cortex should generate tens of billions of spikes a second
  - Thousands of synaptic updates per spike
  - Hence the need for 228 trillion synaptic updates per second

- 228 trillion synaptic operations per second
- Energy consumption 40 kW
- Still 2000 times more than the human brain!

• Is this true?



# Dark Matter?

PROCEEDINGS OF THE IEEE, VOL. 56, NO. 6, JUNE 1968

## The Electrical Properties of Metal Microelectrodes

DAVID A. ROBINSON, MEMBER, IEEE

Looked at another way, in a 2-mm electrode track the tip should record from 70 to 234 cells, depending on cell density. In actual practice, in gray matter, one sees only a tiny fraction of these cells, and why this is so is a very disturbing question to users of microelectrodes.

Shy Shoham · Daniel H. O'Connor · Ronen Segev

## How silent is the brain: is there a “dark matter” problem in neuroscience?

J Comp Physiol A (2006) 192: 777–784

## The Cost of Cortical Computation

Peter Lennie\*  
Center for Neural Science  
New York University

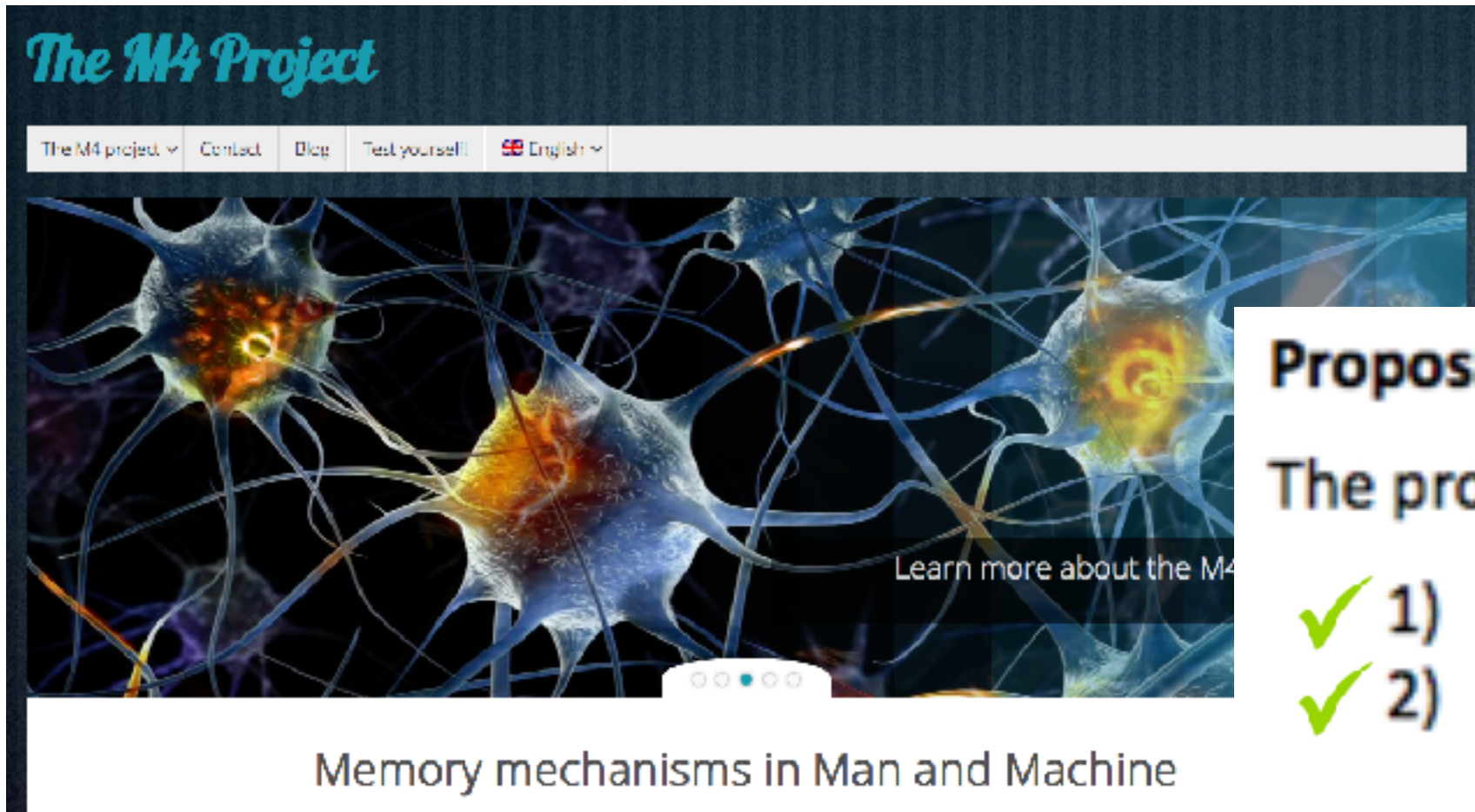
Current Biology, Vol. 13, 493–497, March 18, 2003, (

a single spike consumes  $2.2 \times 10^9$  ATP molecules. Given this, and  $1.9 \times 10^{10}$  cortical neurons, the ATP available for the Na/K pump would support an average discharge rate of 0.16 spikes/s/neuron.

- Have we completely overestimated the actual firing rates in the brain?



# ERC Grant (2013-19)



ERC Proof of Concept Lump Sum Pilot Grant 2019

Part B

*TeraBrain : Simulating a spiking neural network with a trillion neurons*

## Proposal summary

The project aims to validate a set of 10 provocative claims.

- ✓ 1) Humans can recognise visual and auditory stimuli that they have not experienced for decades.
- ✓ 2) Recognition after very long delays is possible without ever reactivating the memory trace in the intervening period.
- ✓ 3) These very long term memories require an initial memorisation phase, during which memory strength increases roughly linearly with the number of presentations
- ✓ 4) A few ~~tens of~~ presentations can be enough to form a memory that can last a lifetime.
- 5) Attention-related oscillatory brain activity can help store memories efficiently and rapidly
- ✓ 6) Storing such very long-term memories involves the creation of highly selective "Grandmother Cells" that only fire if the original training stimulus is experienced again.
- ✓ 7) The neocortex contains large numbers of totally silent cells ("Neocortical Dark Matter") that constitute the long-term memory store. **Jury still out!**
- ✓ 8) Grandmother Cells can be produced using simple spiking neural network models with Spike-Time Dependent Plasticity (STDP) and competitive inhibitory lateral connections.
- ✓ 9) This selectivity only requires binary synaptic weights that are either "on" or "off", greatly simplifying the problem of maintaining the memory over long periods. **Not binary - unary!**
- ✓ 10) Artificial systems ~~using memristor-like devices~~ can implement the same principles, allowing the development of powerful new processing architectures ~~that could replace conventional computing hardware.~~





# Takehome messages

## Impact of studies of the brain on AI

## Temporal constraints on processing (1989, 1996...)

- The need for feedforward architectures
- ImageNet Challenge (2009)
- AlexNet (2012)
- The Deep Learning revolution

## Spikes!

- Coding
  - Order based coding
  - N of M coding
  - Unitary weights
- TeraBrain architectures
  - Zeroless computing
  - Ultra sparse activity
  - Dark Matter?
- Key to understanding the 20 watt power budget

Bloomberg

## OpenAI's First Stargate Site to Hold Up to 400,000 Nvidia Chips


Nation's first Stargate data center in West Texas is already in expansion mode  
Work on second phase gets underway



A rendering of the data center campus, commonly known as the first Stargate Project, in Abilene, Texas, being developed by Crusoe Energy. (Crusoe Energy)

### Stargate's First AI Data Center Site Is Taking Shape

Satellite imagery shows the first two buildings of a planned data center campus have been constructed in Abilene, Texas.

<p>June 6, 2024</p>  <p>Planned site of data centers</p>	<p>March 4, 2025</p>  <p>First two buildings are built and six more are planned</p>
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Abilene, TX

1000 ft  
200 m

Sources: Planet Labs; DC Byte

Bloomberg

# AI still has a lot to learn from the brain!



**Thank you!**