Neuromorphic Computing with BrainScaleS

Accelerated Analog Spiking Neural Network Emulation

Eric Müller mueller@kip.uni-heidelberg.de

EINC, Heidelberg University

2022-11-08







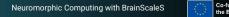


BrainScaleS-2

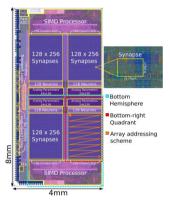


- Physical model (mixed-signal) with accelerated model dynamics ($\sim 10^3$)
- AdEx neurons, synapses with correlation sensors
- Support for 'online' updates of neuron parameters, synapses and network topology
- Programmable plasticity (via embedded SIMD processors and per-synapse observables)
- Structured neurons & nonlinear effects of dendrites
- Non-spiking operation mode





BrainScaleS-2



- Physical model (mixed-signal) with accelerated model dynamics (~ 10³)
- AdEx neurons, synapses with correlation sensors
- Support for 'online' updates of neuron parameters, synapses and network topology
- Programmable plasticity (via embedded SIMD processors and per-synapse observables)
- Structured neurons & nonlinear effects of dendrites
- Non-spiking operation mode





BrainScaleS-2 Systems





Hardware setups

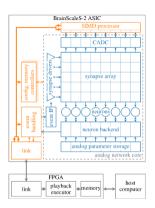
- Single-chip lab systems local and remote users
- (Mobile systems embedded operation)
- (Multi-chip systems under development)
- → "Network-attached accelerators"
 - Abstraction levels in software:
 - PyNN.brainscales (and hxtorch.snn)
 - Lower-level layers for configuration and control
 - $\,\rightarrow\,$ APIs for modeling, commissioning and development
- Time-shared hardware resource
 - Experiment service for interactive (O(10 ms)) access from Collab (& soon federated resources/HPC)





Neuromorphic "Programming Model"

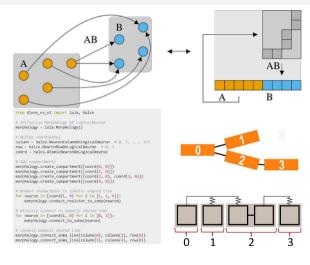
- Neurons & Morphology
- Synapses & Topology
- Stimulus, recorded observables → I/O
- Controllers:
 - Host computer
 - FPGA
 - Embedded processors





Neuromorphic "Programming Model"

- Neurons & Morphology
- Synapses & Topology
- Stimulus, recorded observables → I/O
- Controllers:
 - Host computer
 - FPGA
 - Embedded processors

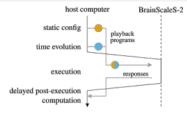


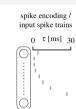




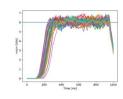
Neuromorphic "Programming Model"

- Neurons & Morphology
- Synapses & Topology
- Stimulus, recorded observables → I/O
- Controllers:
 - Host computer
 - FPGA
 - Embedded processors





synapse_type = MomeostaticSynapse(timer=timer, target=60, weightpynn:Projection(pop_input, nrn, pynn.AllToAllConnector(), synapse_type=synapse_type)



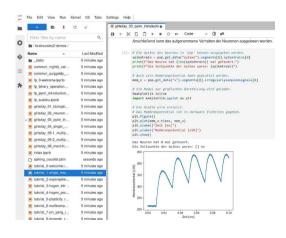




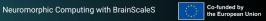
Summary

- Interactive access to accelerated neuromorphic BrainScaleS via EBRAINS
- → Open research platform
 - Join the hands-on session! Thu, 2022-11-10 13-14 CET (12-13 UTC)









Thank you!





